Design of Second Order Recursive Digital Integrators with Matching Phase and Magnitude Response

Kriti GARG, Dharmendra K. UPADHYAY

Dept. of Electronics and Communication Engineering, Netaji Subhas Institute of Technology, Sector-3, Dwarka, 110078 New Delhi, India

kritigarg13@gmail.com, upadhyay_d@rediffmail.com

Submitted April 21, 2016 / Accepted August 23, 2016

Abstract. Location of poles and zeroes greatly affect phase response and magnitude response of a system. Recently, pole-zero optimization emerged as an effective approach to approximately match magnitude response of a system with that of an ideal one. In this brief, a methodology for the design of linear phase integrators and ones with constant phase of -90 degree is proposed.

The aim of this method is to simultaneously attain multiple objectives of magnitude and phase optimization. In this method, magnitude response error is minimized under the constraint that the maximum passband phase-response error is below a prescribed level. Examples are included to illustrate the proposed design technique.

Keywords

Digital integrator, genetic algorithm, linear phase, polezero optimization, recursive, simulated annealing

1. Introduction

An integrator is a system whose output signal is the time integral of its input signal. It can be mathematically modelled as,

$$H_i(\omega) = 1/j\omega$$

where $j = \sqrt{-1}$ and ω is the angular frequency in radians per second. Integrators find immense applications in the areas of signal processing, bio-medical engineering, radar engineering, sonar engineering, control system etc. Multitude of techniques have been developed for the design of recursive and non-recursive digital integrators. It is interesting to note that for linear phase integrators, Maximum Percentage Relative Error (MPRE) in magnitude response has rolled down from 5 % to 0.48 % in the decade 2005 – 2015.

Several designs of digital integrator have been proposed in the literature [1–7]. Among these, the simplest approximations to the desired frequency response are Rectangular, Trapezoidal and Simpson digital integrators. It can be easily

DOI: 10.13164/re.2017.0376

noticed that, as $\omega \to 0$, $H(e^{j\omega}) \to 1/j\omega$ for these approximations. These digital integrators can be sufficient for the integration of oversampled signals (signals sampled much above the Nyquist rate), but a deeper investigation suggests that there is still a possibility of better designs for signals that are critically sampled at the Nyquist rate.

Digital integrators have been designed using quadrature rules, such as the Newton-Cotes and Gauss- Legendre rules by Ngo and Tseng [3–6]. These methods, however, are complex to design and implement due to the usage of fractional sampling rates, hence lacking computational efficiency. Lagrange interpolators have been suggested to elevate some of these problems. Most recently Tseng et al. have proposed to implement the fractional delays in the Hartley transform domain [7].

One of the design methods which has become very popular among researchers is Iterative Optimization Method [8-20]. It is widely used to improve the performance of a system by reducing its runtime, bandwidth, memory requirement, or some other property. Optimization methods such as Linear Programming, Simulated Annealing, Genetic Algorithm, and Pole-Zero optimization have been used earlier to design Infinite Impulse Response (IIR) digital integrators and differentiators. Papamarkos-Chamzas [8] have used Linear Programming optimization method to design digital integrators. Al-Alaoui [9] has also proposed a family of digital integrators by using interpolation and Simulated Annealing optimization method. Upadhyay-Singh (US) have proposed recursive wideband digital integrator for 0.48 % MPREs in magnitude responses over almost the full Nyquist band except near to $\omega = \pi$ [10]. Genetic Algorithm has been exploited in [16] to obtain a class of second order linear phase integrators. In [17], Jain-Gupta-Jain have used Minimax and Pole, Zero, and Constant Optimization Methods to obtain second, third and fourth order IIR digital integrators. Later, in [18], Gupta et al. (GET) have proposed recursive wideband digital integrators using Modified Particle Swarm Optimization (MPSO). In [19], Jalloul and Al-Alaoui have employed Particle Swarm Optimization to propose designs of integrators and differentiators. Upadhyay, in [20], has used Pole-Zero Optimization to achieve integrators with relative

error in magnitude response not exceeding 0.37 %. In [21], efficient design of FIR digital differentiator using the L_1 -optimality criterion is proposed.

Often application in controls, wave shaping, oscillators and communication require a constant 90° phase for differentiators and -90° phase for integrators. In [22], Al-Alaoui cascaded differentiator and integrator operator with fractional advance and delay respectively to obtain constant 90° phase for differentiators and constant -90° phase for integrators. Al-Alaoui also showed that doubling the sampling rate improves the magnitude response. Combining the two actions improves both the magnitude and phase responses. It should be noted that the approach applies to other differentiators and integrators with linear phases, or approximately linear phases, such as the second-order Al-Alaoui integrators and differentiators. However, the approach is limited only to linear phase differentiator and integrator. Also, fractional delays and advances are complex to implement. In this brief, our goal is to improve the performance and computational efficiency of digital integrators as a standalone system.

Iterative constrained optimization has already been proven a superior method of obtaining close-to-perfect magnitude responses. However, cost function defined in these methods only considers magnitude error with no account of phase error. For real-time applications, further reduction in magnitude error is not as important as reduction in phase error is.

Every recursive digital filter can be completely specified by its poles and zeros with suitable scaling. Poles and zeros give useful insights into a filter's frequency response, and can be used as the basis for digital filter design. This paper attempts at developing a design method which governs constraints on the location of poles and zeroes in order to achieve multi-objectives of phase and magnitude responses, both together.

The rest of the paper is organized as follows. Section 2 gives a brief account of problem under consideration. Solution Methodology is developed in Sec. 3. Design Steps are presented in Sec. 4. Design Examples are given in Sec. 5 of the paper. In Sec. 6, Performance Results are elucidated. Conclusions are drawn in Sec. 7.

2. Problem Formulation

The paper addresses two design objectives. Firstly, the design of stable recursive linear phase digital integrator and secondly, the design of stable recursive digital integrator with a constant phase of -90° . These designs, however, involve non-converging objectives of magnitude and phase optimization and there exist a trade-off among the different objectives. The trade-off parameters considered in the designs are: passband magnitude error and passband phase deviation, passband cut-off frequency in phase response (ω_p): which is defined as range of frequency for which phase deviation $\leq \delta$.

Design objectives for linear phase integrators can hence be summarized as:

- 1. Low wideband magnitude error
- 2. Low passband phase deviation (constant group delay)

Design objectives for integrators with constant phase of -90° are given below:

- 1. Low wideband magnitude error
- 2. High Passband cut-off frequency in phase response
- 3. Low passband phase deviation

Let, $H_l(z)$ denotes the transfer function of linear phase integrator and $H_c(z)$ denotes the transfer function of integrator with constant phase of -90° .

A generalized digital recursive transfer function of second order is of the form, given in (1).

$$I(z) = I_o \frac{(z - z_1)(z - z_2)}{(z - p_1)(z - p_2)}$$
(1)

where z_1, z_2 are zeroes and p_1, p_2 are poles of the considered transfer function. Here, I_o is a multiplier constant or scaling factor. The frequency response $I(e^{j\omega})$ of the considered system can be written as in (2),

$$(e^{j\omega}) = |I(e^{j\omega})|e^{j\theta(\omega)} = I_o \frac{\prod_{i=1}^2 |I_i(e^{j\omega})|e^{j\phi_i(\omega)}}{\prod_{i=3}^4 |I_i(e^{j\omega})|e^{j\phi_i(\omega)}}$$
(2)

where $|I(e^{j\omega})|$ and $\theta(\omega)$ are the magnitude and phase response of the digital integrator. $|I_1(e^{j\omega})|, |I_2(e^{j\omega})|, |I_3(e^{j\omega})|, |I_4(e^{j\omega})|$ and $\phi_1(\omega), \phi_2(\omega), \phi_3(\omega), \phi_4(\omega)$ are the magnitude responses and phase characteristics of z_1, z_2, p_1, p_2 respectively. Now, phase response of the overall transfer function is given as, $\theta(\omega) = \phi_1(\omega) + \phi_2(\omega) - \phi_3(\omega) - \phi_4(\omega)$.

Error function to be minimized is given in (3).

$$E = \int_0^{\pi} \left(\left| I(e^{j\omega}) \right| - \frac{1}{\omega} \right)^2 d\omega.$$
 (3)

The main reason for expressing the magnitude constraints in terms of magnitude-square instead of magnitude is to avoid the appearance of a denominator for the gradients of the magnitude constraints, which the use of the simple magnitude would have made unavoidable.

A necessary and sufficient condition for the stability of a causal Linear Time Invariant (LTI) digital IIR filter is that all poles of its irreducible transfer function lie strictly inside the unit circle.

3. Solution Methodology

Consider a polynomial equation x = (z - a), where $a \in \mathbf{R}$. Substituting $z = e^{j\omega}$, we get phase of x, given in (4).

$$\phi_x(\omega) = \tan^{-1} \frac{\sin \omega}{\cos \omega - a}.$$
 (4)

.



Fig. 1. Illustration of phase effects of zeroes and poles to obtain linear phase characteristics for a system.

location of root	$\phi_x(\omega) _{\omega=0}$	$\phi_x(\omega) _{\omega=\pi}$
<i>a</i> > 1	π	0
a < -1	0	$-\pi$
a = 1	$\pi/2$	0
a = -1	0	$-\pi/2$

Tab. 1. Values of $\phi_x(\omega)$ for different root locations.

Groupdelay of *x* is defined as:

$$\tau_x(\omega) = -\frac{\mathrm{d}\phi_x(\omega)}{\mathrm{d}\omega} = -\frac{1 - a\cos\omega}{(1 - a\cos\omega) + a(a - \cos\omega)}.$$
 (5a)

From (5a), it is noted that:

$$\lim_{a \to \infty} \tau_x(\omega) = 0.$$
 (5b)

Therefore, indicating that greater the value of *a*, better the phase linearity of the system.

We next require to compute values of $\phi_x(\omega)$ at extreme frequency points in the Nyquist Band (i.e. at $\omega = 0$ and at $\omega = \pi$) for different locations of root. Table 1 summarizes values of $\phi_x(\omega)$ for different root locations.

3.1 Design of Linear Phase Integrators

Figure 1 illustrates the phase effects of zeroes and poles to obtain linear phase characteristics for a discrete time system. It is observed from this figure that in order to have a linear phase response for a system with transfer function $H_l(z)$, phase function of one of the poles p_1 , i.e., $\phi_3(\omega)$ should be equal to 90° at $\omega = 0$ and decrease monotonically henceforth with constant slope for the complete range of Nyquist frequency. Another pole p_2 should be located strictly inside the unit circle to satisfy stability constraints for the system. One of the zeroes z_2 should be so chosen that it neutralizes the phase effects of p_2 . The phase response of zero z_1 , i.e, $\phi_1(\omega)$ is required to be monotonically decreasing, linear function of frequency with slope much greater than that of $\phi_3(\omega)$, so that, when phase characteristic of p_1 is subtracted from that of z_1 , i.e., $\phi_1(\omega) - \phi_3(\omega)$, linear phase characteristic is obtained for the overall transfer function $H_l(z)$.

Based on this, required phase response of p_1 can be mathematically modelled by the following conditions:

1.
$$\phi_3(\omega)|_{(\omega=0)} = \pi/2,$$

2. $\phi_3(\omega)|_{(\omega=\pi)} = 0,$
3. $\left|\frac{d}{d\omega}\phi_3(\omega)\right| = \frac{1}{2}.$

From Tab. 1, it is observed that for pole location z = 1, first two of the above mentioned conditions are fulfilled.

Differentiating $\phi_3(\omega)$, we get

$$\frac{\mathrm{d}\phi_3(\omega)}{\mathrm{d}\omega} = \frac{1 - a\cos\omega}{1 + a^2 - 2a\cos\omega}$$

Put a = 1

$$\left|\frac{\mathrm{d}\phi_3(\omega)}{\mathrm{d}\omega}\right| = \frac{1}{2}$$

which satisfies the third condition.

Hence, it is concluded that pole p_1 is located at z = 1. Isolated poles on the unit circle may be called marginally stable. The impulse response corresponding to a single pole on the unit circle never decays, but neither does it grow.

Next, constraints on phase function of z_1 are described below:

1.
$$\phi_1(\omega)|_{(\omega=0)} = 0$$
,

2. $\tau_1(\omega) = \text{constant}$ (for phase linearity).

For the first condition to hold true, zero z_1 can take any value in the interval $(-\infty, -1)$, as concluded from Tab. 1. Assume z_1 lies in the interval $(-\alpha, -1)$. Solution for α can be obtained by solving the equation:

$$\tau_x(\omega)|_{x=\alpha,\omega=0} = \kappa \qquad \qquad \kappa \in (0,1), \tag{6}$$

 $\kappa = 1$ results into a perfect linear phase response while $\kappa = 0$ leads to a constant phase response of -90° .

Different solutions of α can be obtained for different defined values of κ .

An LTI filter is stable if and only if its poles are strictly inside the unit circle (|z| = 1) in the complex z plane. In particular, a pole p outside the unit circle (|p| > 1) gives rise to an impulse-response component proportional to p^n which grows exponentially over time n. Therefore, pole p_2 must lie in the interval (-1,1). To achieve linear phase characteristics for the system, p_2 and z_2 should lie close enough to each other. Let Δp be the distance between the two. For faithful neutralization of the two phase effects, $|\Delta p|$ should not exceed 0.1

In conclusion, to obtain linear phase characteristics for a second order system, p_1 should be located at z = 1. p_2 should lie in the interval (-1,1). $z_2 = p_2 + |\Delta p|$, where $|\Delta p|$ does not exceed 0.1. z_1 should lie in the interval $(-\alpha, -1)$ where α is linearly related to user-defined parameter κ .

3.2 Design of Integrators with Constant Phase of -90°

Now that we have estimated location of poles and zeroes of $H_l(z)$ to obtain linear phase characteristics for the system, we would now like to investigate location of poles and zeroes to achieve a constant phase response of -90° for a system with transfer function $H_c(z)$. Let z'_1 , z'_2 , p'_1 and p'_2 denote the zeroes and poles of $H_c(z)$.

From Fig. 1, it is observed that a positive angular shift of 26.56° in phase response of $H_l(z)$ causes its linear phase characteristics to transform into a constant phase characteristics of -90° . It can henceforth be argued that cumulative sum of angular phase shifts contributed by poles and zeroes of $H_c(z)$ should be equal to 26.56°.

Let,

- 1. Δ_1 be the positive angular phase shift caused by z'_1 , measured w.r.t $\phi_1(\omega)$ (see Fig. 1)
- 2. Δ_2 be the negative phase shift caused by z'_2 , measured w.r.t frequency axis.
- 3. Δ_3 be the negative phase shift caused by p'_1 , measured w.r.t $-\phi_3(\omega)$.
- 4. Δ_4 be the positive phase shift caused by p'_2 , measured w.r.t frequency axis.

It is to be noted that phase function of pole p'_1 suffers the same constraints as p_1 and it continues to be located at z = 1. Hence, $\Delta_3 = 0$.

Now, summation of phase shifts caused by z'_1 , z'_2 and p'_2 should be equal to 26.56°, given by (7),

$$|\Delta_1| - |\Delta_2| + |\Delta_4| = 26.56^\circ + \Delta \tag{7}$$

where Δ is tolerable angular phase shift. At first, consider $|\Delta_2|$ and Δ small enough to be neglected. So, (7) becomes,

$$|\Delta_1| + |\Delta_4| = 26.56^{\circ}. \tag{8a}$$

In (8a), $z'_1 \ge -1$. So, maximum value of Δ_1 is achieved when z'_1 is located at z = -1.

$$\max(|\Delta_1|) = 18.44^{\circ} \tag{8b}$$

This implies that

$$\min(|\Delta_4|) = 8.12^{\circ}.$$
 (8c)

Relationship between root location a and corresponding maximum angular phase deviation (Δ) caused by it is derived as follows:

Maximum Angular Phase Deviation (Δ) can be defined as slope of a tangent to the phase response curve at $\omega = 0$. By above definition,

$$\left. \frac{\mathrm{d}\phi_x(\omega)}{\mathrm{d}\omega} \right|_{\omega=0} = \Delta. \tag{9}$$

Similarly, relationship between root location *a* and corresponding maximum deviation caused (δ) by it is derived as follows:

Maximum Phase Deviation (δ) can be defined as the value of phase obtained at a stationary point on the phase curve at which the tangent changes from a positive value on the left of this point to a negative value on the right.

Solving for ω in the equation $\frac{d\phi_x(\omega)}{d\omega} = 0$ we get

$$\frac{1 - a\cos\omega}{1 + a^2 - 2a\cos\omega} = 0$$
$$\Rightarrow a = \frac{1}{\cos\omega}$$
$$\Rightarrow \omega = \cos^{-1}(1/a).$$

 $\phi_x(\omega)|_{\omega=\cos^{-1}(1/a)} = \delta.$

By definition

This gives us

$$\tan^{-1} \frac{\sin(\cos^{-1}(1/a))}{(1/a) - a} = \delta.$$
 (10b)

Solution of (9) and (10b) gives relationship between Δ and δ .

For $\Delta = 8.12^{\circ}$, solving (9) *a* comes out to be -0.3. This implies that pole p'_2 is located at z = -0.3

As location of zero z'_1 goes beyond z = -1 (towards $-\infty$), $|\Delta_1|$ decreases requiring $|\Delta_4|$ to increase and pole to shift beyond z = -0.4 (towards -1). Equation (8) suggests that $|\Delta_4|$ can not exceed 26.56°. This marks maximum bound on $|\Delta_4|$. This is achieved when pole p'_2 is located at z = -0.6. Hence, pole p'_2 is restricted to be located in the interval (-0.6, -0.3).

Practically, neither Δ nor $|\Delta_2|$ can be 0. From (7), we conclude the following:

$$ax(|\Delta_1| - |\Delta_2|) = 18^\circ + \Delta, \tag{11}$$

$$\min(|\Delta_1| - |\Delta_2|) = \Delta. \tag{12}$$

From above analysis, we investigate that p'_1 is located at z = 1. p'_2 lies in the interval (-0.6, -0.3). z'_1 lies in the interval (- β , -1) and z'_2 lies in the interval (- γ , 0).

For different values of δ (and hence Δ) chosen, different solution sets for (β, γ) can be derived.

4. Design Approach

m

The typical approach in designing of a digital IIR integrator is to minimize the maximum amplitude-response error and maximum phase-response error. The optimization can be carried out by minimizing the magnitude error under the constraint that the passband phase error δ is within prescribed level.

Iterative optimization methods are often the only choice for non linear equations. It is a mathematical procedure that

(10a)

generates a sequence of improving approximate solutions until convergence is reached. Genetic Algorithm (GA) is already used in the literature for designing digital differentiators and integrators [13]. In this work, GA is used in a similar fashion as in [13]. However, the optimization works on optimizing the gain factor and the locations of poles and zeroes of the digital filter as opposed to optimizing the coefficients of the numerator and the denominator.

Steps followed in the design of $H_l(z)$ and $H_c(z)$ are given below:

Step 1: For the design of $H_l(z)$, define a value of κ . Then, solution of (6) gives the value of α .

For the design of $H_c(z)$, choose a required value for δ . Simultaneous solution of (9), (10) and (11) yields solution set for (β, γ) .

Step 2: Run the iterative optimization with poles and zeroes set to vary within the respective prescribed intervals for both the designs, as discussed in previous section. Cost function is given in (3). The optimization runs until convergence is reached.

The constraints on the location of poles and zeroes ensure that the phase-response error is under a prescribed level. The frequency range is defined as $0 \le \omega \le \pi$ radians/second.

5. Design Examples

In real world applications, most of the optimization problems involve more than one objective to be optimized. The objectives in most of engineering problems are often conflicting. In the case, one extreme solution would not satisfy both objective functions and the optimal solution of one objective will not necessarily be the best solution for other objective(s). Therefore, different solutions will produce trade-off between different objectives and a set of solutions is required to represent the optimal solutions of all objectives. Several design examples are included which demonstrate the effectiveness of the design technique.

5.1 Linear Phase Integrators

In this section, three designs of linear phase digital integrators have been considered for different values of κ . Figures 2 and 3 illustrate the magnitude and phase responses of proposed integrators $H_{l1-l3}(z)$.

Example 1:
$$\kappa = 0.8$$

Solving (6) for $\kappa = 0.8$, α comes out to be 5. Proposed integrator is given in (13).

$$H_{l1}(z) = 0.1518 \frac{z^2 + 5.4866z + 2.5897}{z^2 - 0.4521z - 0.5479}.$$
 (13)

The design has poor magnitude response and lacks phase linearity.



Fig. 2. Magnitude responses of proposed integrators $H_{l1-l3}(z)$.



Fig. 3. Phase responses of proposed integrators $H_{l1-l3}(z)$.

Example 2: $\kappa = 0.95$

Solving (6) for $\kappa = 0.95$, α comes out to be 20. Proposed integrator is given in (14).

$$H_{l2}(z) = 0.05578 \frac{z^2 + 15.5885z + 8.4692}{z^2 - 0.5361z - 0.4639}.$$
 (14)

The design out performs other linear phase design examples proposed in this section in terms of phase linearity over the full Nyquist band and has reasonably well magnitude response for complete Nyquist frequency range.

Example 3: $\kappa = 0.9$

Solving (6) for $\kappa = 0.9$, α comes out to be 10. Proposed integrator is given in (15).

$$H_{I3}(z) = 0.08504 \frac{z^2 + 10.5789z + 5.8587}{z^2 - 0.4929z - 0.5071}.$$
 (15)



Fig. 4. Magnitude responses of proposed integrators $H_{c1-c3}(z)$.



Fig. 5. Phase responses of proposed integrators $H_{c1-c3}(z)$.

The design outperforms other design examples in terms of magnitude response and also has considerably good phase response over wideband.

5.2 Integrators with Constant Phase of -90°

This section demonstrates three designs of integrators with constant phase of -90° for different values of tolerable maximum phase deviation δ . Figures 4 and 5 show the magnitude and phase responses of designed integrator $H_{c1-c3}(z)$.

Example 1:
$$\delta = 2^{\circ}$$

For $\delta = 2^{\circ}$, β and γ are found to be -1.05 and -0.6 respectively. Proposed integrator is given in (16).

$$H_{c1}(z) = 0.7679 \frac{z^2 + 1.5715z + 0.5560}{z^2 - 0.4204z - 0.5796}.$$
 (16)

Phase deviation for this design does not exceed 2° in the frequency range $0 \le \omega \le 0.88\pi$. So, cut-off frequency in phase response (ω_p) turns out to be 0.88π . The design, however, can not be considered magnitude efficient with Maximum Magnitude Relative Error (MMRE) of 0.5025 over wideband.

Example 2: $\delta = 5^{\circ}$

For $\delta = 5^{\circ}$, β and γ come out to be -1.9 and -0.2 respectively. Proposed integrator is given in (17).

$$H_{c2}(z) = 0.5089 \frac{z^2 + 1.9341z + 0.2312}{z^2 - 0.4902z - 0.5098}.$$
 (17)

The design has considerably accurate magnitude response within $\delta = 5^{\circ}$ in phase response in the frequency range $0 \le \omega \le 0.69\pi$. Here, ω_p comes out to be 0.69π , making it attractive for real-time applications.

Example 3: $\delta = 10^{\circ}$

For $\delta = 10^\circ$, solution of β and γ are found to be -2.3 and -1.8. Proposed integrator is given in (18).

$$H_{c3}(z) = 0.4857 \frac{z^2 + 1.8514z + 0.1751}{z^2 - 0.4976z - 0.5024}.$$
 (18)

The design excels in magnitude response and has $\delta = 10^{\circ}$ in phase response in the frequency range $0 \le \omega \le 0.76\pi$. ω_p for this design is 0.76π

Though, it is disappointing that none of the design example achieves a constant phase response of -90° over full Nyquist band, it is worth noticing that passband cut-off frequency ω_p or bandwidth of the system largely depends on location of zero z'_1 . The dependence is depicted below:

$$\phi_1(\omega)|_{\omega=\omega_n} = -90^\circ.$$

For a given location of z'_1 , approximate passband for the system could be predicted. Conversely, location of z'_1 could be determined for a prescribed value of ω_p .

6. Performance Measure

SA is another popular optimization technique that is widely used in the literature for obtaining designs for integrators and differentiators [9]. In this work, SA is implemented to simulate integrators proposed in this section. $H_{l1}(z)$ and $H_{c2-c3}(z)$ are used as initial guess in the optimization algorithm.

Designs of digital integrators proposed using SA are of the general form, as given in (19).

$$H(z) = k_o \frac{z^2 + a_o z + a_1}{z^2 + b_o z + b_1}.$$
 (19)

Proposed linear phase integrators are tabulated in Tab. 2 and ones with constant phase of -90° are tabulated in Tab. 3.

To verify the accuracy of proposed linear phase integrators, recently published Jain-Gupta-Jain [16], $H_{2MO+PZC}(z)$ [17], Gupta et al. [18] and Upadhyay recursive digital integrators are considered. Their transfer functions are given in (20–23), respectively.

Integrators	k _o	a_o	a_1	b_o	b_1
$H_{l4}(z)$	0.09522	9.66190	5.19090	-0.49750	-0.50250
$H_{l5}(z)$	0.08515	10.57370	5.81810	-0.50220	-0.49780
$H_{l6}(z)$	0.08506	10.54160	5.48710	-0.54160	-0.45840
$H_{l7}(z)$	0.08860	10.07480	5.39990	-0.48330	-0.51670

Tab. 2. Coefficients of proposed linear phase integrators obtained using SA.

Integrators	k _o	a _o	a_1	b_o	b_1
$H_{c4}(z)$	0.5068	1.8037	0.1678	-0.4924	-0.5076
$H_{c5}(z)$	0.4875	1.8312	-0.5032	0.2274	-0.4968
$H_{c6}(z)$	0.5246	1.9445	0.2629	-0.4488	-0.5512

Tab. 3. Coefficients of proposed integrators with constant phase of -90 degree obtained using SA.



Fig. 6. Magnitude responses of proposed and the existing linear phase integrators with the ideal integrator.

$$H_{\rm JGJ}(z) = 0.0868 \frac{z^2 + 0.9148z + 0.5122}{z^2 - 0.4881z - 0.5107},$$
 (20)

$$H_{2\text{MO+PZC}}(z) = 0.8655 \frac{z^2 + 0.6933z + 0.0623}{z^2 - 0.4834z - 0.5151},$$
 (21)

$$H_{\text{get}}(z) = \frac{0.0901z^2 + 0.9216z + 0.5429}{z^2 - 0.4445z - 0.5543},$$
 (22)

$$H_{\rm up}(z) = 0.8642 \frac{z^2 + 0.6848z + 0.0577}{z^2 - 0.4930z - 0.5070}.$$
 (23)

And as a comparison benchmark for proposed recursive integrators with constant phase of -90° , Ngo [3] and Tseng [5] recursive digital integrators are considered. Their transfer functions are given in (24) and (25), respectively.

$$H_{\text{Ngo}}(z) = \frac{9 + 19z^{-1} - 5z^{-2} + z^{-3}}{24(1 - z^{-1})},$$
 (24)



Fig. 7. Phase responses of proposed and the existing linear phase integrators with the ideal integrator.

Integrators	Max δ	max MRE
$H_{l3}(z)$	3.6089	0.0273
$H_{l4}(z)$	3.4775	0.0433
$H_{l5}(z)$	3.9078	0.0341
$H_{l6}(z)$	3.7995	0.0398
$H_{l7}(z)$	3.8579	0.0580
$H_{\rm JGJ}(z)$	38.4682	0.2153
$H_{2MO+PZC}(z)$	45.0071	0.2909
$H_{\text{get}}(z)$	37.6768	0.2082
$H_{up}(z)$	10.4348	0.0149

Tab. 4. Max Phase Deviation (in degrees) and Max MRE of linear phase integrators.

$$H_{\text{Tseng}}(z) = z \frac{-3693 + 67260z^{-1} + 88650z^{-2} - 14388z^{-3} + 2139z^{-4}}{139968(1 - z^{-1})}.$$
(25)

Figures 6 and 7 show the magnitude and phase responses of proposed linear phase integrators $H_{l3-l7}(z)$, $H_{JGJ}(z)$, $H_{2MO+PZC}(z)$, $H_{get}(z)$, $H_{up}(z)$ digital integrator with the



Fig. 8. Magnitude responses of proposed and the existing -90° phase integrators with the ideal integrator.



Fig. 9. Phase responses of proposed and the existing -90° phase integrators with the ideal integrator.



Fig. 10. Relative Magnitude Errors of proposed and the existing linear phase integrators.

ideal one for the complete Nyquist frequency range, respectively. Further, Figures 8 and 9 show the magnitude and phase responses of proposed integrators with constant phase of $-90^{\circ} H_{c2-c6}(z)$, Ngo [3] and Tseng [5] digital integrator with the ideal one for the complete Nyquist frequency range, respectively.



Fig. 11. Relative Phase Errors of proposed and the existing linear phase integrators.



Fig. 12. Relative Magnitude Errors of proposed and the existing -90° phase integrators.



Fig. 13. Relative Phase Errors of proposed and the existing -90° phase integrators.

For proposed and existing linear phase integrators, δ and MMRE, over full Nyquist band, are given in Tab. 4 and that for proposed and existing integrators with constant phase of -90° , for $0 \le \omega \le 0.71\pi$ and $0 \le \omega \le 0.76\pi$ are given in Tab. 5.

Integrators	Max $\delta(\omega_c = 0.71\pi)$	Max $\delta(\omega_c = 0.76\pi)$	Max MRE
$H_{c2}(z)$	5.9806	12.1286	0.1460
$H_{c3}(z)$	6.0427	10.0212	0.0367
$H_{c4}(z)$	6.8929	8.3278	0.0279
$H_{c5}(z)$	4.6879	10.5810	0.1082
$H_{c6}(z)$	4.9046	9.6289	0.2514
$H_{\rm Ngo}(z)$	27.0425	34.8119	0.0643
$H_{\rm Tseng}(z)$	12.5687	17.1973	0.6180

Tab. 5. Max Phase Deviation and Max MRE of Integrators with constant phase of -90 degree.

Figures 10 and 11 show plots of the variation of magnitude error function and phase error function with frequency for the proposed linear phase integrators $H_{l3-l7}(z)$, $H_{JGJ}(z)$, $H_{2MO+PZC}(z)$, $H_{get}(z)$, $H_{up}(z)$ digital integrators

Figures 12 and 13 show plots of the variation of magnitude error function and phase error function with frequency for integrators with constant phase of -90° designed using our method $H_{c2-c6}(z)$, Ngo and Tseng integrator.

From Figures 10 and 11, it is observed that all the proposed linear phase integrator designs $H_{l3-l7}(z)$ have an MMRE not more than 0.06 and maximum phase deviation less than 4° over wideband. All these designs clearly outperform any of the existing integrators in phase response over complete Nyquist range. $H_{JGJ}(z)$, $H_{2MO+PZC}(z)$, $H_{get}(z)$ and $H_{up}(z)$ have a maximum phase deviation of 38.4682°, 45.0071°, 37.6768° and 10.4348° for $0 \le \omega \le \pi$ and an MMRE of 0.2153, 0.2909, 0.2082 and 0.0149, respectively.

 $H_{l3}(z)$ has an MMRE of 0.0273 over wideband, better than the existing or proposed integrator designs. It exhibits a maximum phase deviation of just 3.6089° over complete Nyquist range. $H_{l4}(z)$ exhibits an MMRE of 0.0433 and a maximum phase deviation of 3.4775°. $H_{l5}(z)$ and $H_{l6}(z)$ have an MMRE < 0.02 for $0 \le \omega \le 0.8\pi$ and an MMRE of 0.0341 and 0.0398 over wideband, respectively. They exhibit a maximum phase deviation of 3.9078° and 3.7995° over complete Nyquist band, respectively. With an MMRE of 0.0580 over wideband, $H_{l7}(z)$ has relatively low magnitude error for $0.7\pi \le \omega \le 0.95\pi$. It exhibits maximum phase deviation of 3.8579° over wideband and outperforms all proposed and existing integrator designs for $0.7\pi \le \omega \le \pi$ in phase response.

With reference to Figures 12 and 13, $H_{\text{Ngo}}(z)$ and $H_{\text{Tseng}}(z)$ perfectly approximate ideal phase response of -90° for ω upto 0.3π after which their phase deviation drastically increases for the rest of the Nyquist band. $H_{\text{Tseng}}(z)$ exhibits excellent magnitude response upto $\omega = 0.3\pi$ but then fails to maintain this for the rest of the Nyquist frequency range. $H_{\text{Ngo}}(z)$, however, has an MMRE of 0.0643 over wideband. All proposed designs $H_{c2-c6}(z)$ have MMRE better than that of $H_{\text{Tseng}}(z)$ over wideband.

 $H_{c4}(z)$ outperforms existing and proposed integrators in magnitude response with an MMRE of just 0.0279 over wideband. It is followed by $H_{c3}(z)$ which has a reasonably well magnitude response for full Nyquist band with an MMRE of 0.0367. These designs, however, do not stand best in phase response but manage to maintain $\delta < 10^{\circ}$ for $0 \le \omega \le 0.77\pi$ and $0 \le \omega \le 0.76\pi$ respectively. These designs could be termed as magnitude-dominant designs.

 $H_{c5}(z)$ and $H_{c2}(z)$ out stand existing and other proposed integrator designs in phase response for $0 \le \omega \le 0.71\pi$ with $\delta < 5^{\circ}$ up to $\omega = 0.71\pi$ and δ approximately equal to 5° upto $\omega = 0.70\pi$, respectively. $H_{c5}(z)$ outperforms $H_{\text{Ngo}}(z)$ for $0 \le \omega \le 0.69\pi$ in magnitude response and exhibits an MMRE of 0.1082 over wideband. $H_{c2}(z)$ lacks in magnitude response with an MMRE of 0.1460 for complete Nyquist band. These designs could be called phase optimal designs.

Amongst proposed integrator designs, $H_{c6}(z)$ exhibits worst magnitude response over wideband and has an MMRE less than 0.1 up to $\omega = 0.8\pi$ and an MMRE of 0.2514 over full Nyquist band. But it is interesting to note that it has $\delta < 5^{\circ}$ for $0 \le \omega \le 0.7\pi$ and $\delta < 10^{\circ}$ for $0 \le \omega \le 0.76\pi$. This can be considered as best trade off between δ and ω_c among all other proposed designs which either have $\delta < 5^{\circ}$ upto $\omega = 0.7\pi$ and $\delta < 10^{\circ}$ for $\omega < 0.76\pi$ or have $\delta < 10^{\circ}$ for $\omega = 0.76\pi$ and $\delta < 5^{\circ}$ for $\omega < 0.7\pi$. It exhibits a unique bandwidth optimality which none of the other design does.

Extensive simulations have been carried out to verify the system's response of proposed integrators but the actual characteristics of the system, in hardware implementation, is governed by the bit-resolution of the processor. The systems coefficients are required to be converted in binary for processing. The coefficients which are multiple of 5 could be represented in binary by finite number of bits while others require infinite number of bits for their binary representation, hence undergoing digital word length effect, owing to which, systems response might worsen. Now a days, 64-bit processors are commonly used for real-time applications. In such a scenario, impact of digital word length effect of coefficients on system's response will be minimal. The poles of all the proposed integrators have been constraint to be located at z = 1, which ensures the response of the proposed integrators match the ideal one near the origin ($\omega = 0$).

7. Conclusion

Methodology for the design of second order linear phase integrators and ones with constant phase of -90° is presented.

Proposed integrators $H_{l_3-l_7}(z)$ show an excellent phase linearity with $\delta < 4^\circ$ over full Nyquist band while simultaneously maintaining low magnitude error. Constant phase of -90° is achieved for proposed integrators $H_{c_2-c_6}(z)$ for $0 \le \omega \le 0.73\pi$ and they exhibit considerably low magnitude error over wideband. Trade-off among magnitude error, phase deviation and passband cut-off frequency is analysed and results show attractive real-time signal processing applications of proposed integrators as per the requirement of accuracy and bandwidth.

The performances of the proposed integrators are compared with few other state of the art integrators and proven of considerable high accuracy. As a future scope of the research, impact of digital word length of the coefficients on the system's response of proposed integrators could be studied further.

References

- KUMAR, B., CHAOUDHURY, D. R., KUMAR, A. On the design of linear phase, FIR integrators for midband frequencies. *IEEE Transactions on Signal Processing*, Oct. 1996, vol. 44, no. 10, p. 2391–2395. DOI: 10.1109/78.539024
- [2] AL-ALAOUI, M. Novel digital integrator and differentiator. *Electronics Letters*, Feb. 1993, vol. 29, no. 4, p. 376–378. DOI: 10.1049/el:19930253
- [3] NGO, N. Q. A new approach for the design of wideband digital integrator and differentiator. *IEEE Transactions on Circuits and Systems II: Express Briefs*, Sep. 2006, vol. 53, no. 9, p. 936–940. DOI: 10.1109/TCSII.2006.881806
- [4] TSENG, C. C. Closed-form design of digital IIR integrators using numerical integration rules and fractional sample delays. *IEEE Transactions on Circuits and Systems I: Regular Papers*, Mar. 2007, vol. 54, no. 3, p. 643–655. DOI: 10.1109/TCSI.2006.887641
- [5] TSENG, C. C., LEE, S. L. Digital IIR integrator design using Richardson extrapolation and fractional delay. *IEEE Transactions on Circuits* and Systems I: Regular Papers, Sep. 2008, vol. 55, no. 8, p. 2300-2309. DOI: 10.1109/TCSI.2008.920099
- [6] TSENG, C. C. Digital integrator design using Simpson rule and fractional delay filter. textitIEE Proceedings - Vision, Image, and Signal Processing, Feb. 2006, vol. 153, no. 1, p. 79–86. DOI: 10.1049/ipvis:20045208
- [7] TSENG, C. C., LEE, S. L. Design of digital IIR integrator using discrete Hartley transform interpolation method. In *Proceedings of the* 2009 IEEE International Symposium on Circuits and Systems. Taipei, May 2009, p. 2181–2184. DOI: 10.1109/ISCAS.2009.5118229
- [8] PAPAMARKOS, N., CHAMZAS, C. A new approach for the design of digital integrators. *IEEE Transactions on Circuits System Part I: Fundamental Theory and Applications*, 1996, vol. 43, no. 9, p. 785-791, DOI: 10.1109/81.536749
- [9] AL-ALAOUI, M. A. Class of digital integrators and differentiators. *IET Signal Processing*, 2011, vol. 5, no. 2, p. 251–260. DOI: 10.1049/iet-spr.2010.0107
- [10] UPADHYAY, D. K., SINGH, R. K. Recursive wideband digital differentiator and integrator. *Electronics Letters*, 2011, vol. 47, no. 11, p. 647–648. DOI: 10.1049/el.2011.0420

- [11] JIANG, A., KWAN, H. K. Minimax design of IIR digital filters using SDP relaxation technique. *IEEE Transactions on Circuits* and Systems I: Regular Papers, 2010, vol. 57, no. 2, p. 378–390. DOI: 10.1109/TCSI.2009.2023770
- [12] LAI, X. P., LIN, Z. P. Minimax design of IIR digital filters using a sequential constrained least-squares method. *IEEE Transactions on Signal Processing*, 2010, vol. 58, no. 7, p. 3901–3906. DOI: 10.1109/TSP.2010.2046899
- [13] ABABNEH, J. I., BATAINEH, M. H. Linear phase FIR filter design using particle swarm optimization and genetic algorithms. *Digital Signal Processing*, Jul. 2008, vol. 18, no. 4, p. 657–668. DOI: 10.1016/j.dsp.2007.05.011
- [14] UPADHYAY, D. K. Recursive wideband digital differentiators. *Electronics Letters*, Dec. 2010, vol. 46, no. 25, p. 1661–1662. DOI: 10.1049/el.2010.2113
- [15] UPADHYAY, D. K. Class of recursive wideband digital differentiators and integrators. *Radioengineering*, Sep. 2012, vol. 21, no. 3, p. 904–910. ISSN: 1805-9600
- [16] JAIN, M., GUPTA, M., JAIN, N. Linear phase second order recursive digital integrators and differentiators. *Radioengineering*, Jun. 2012, vol. 21, no. 2, p. 712–717. ISSN: 1805-9600
- [17] JAIN, M., GUPTA, M., JAIN, N. Analysis and design of digital IIR integrators and differentiators using minimax and pole, zero, and constant optimization methods. *Hindawi Publishing Corporation ISRN Electronics*, vol. 2013, p. 1-14. DOI: 10.1155/2013/493973
- [18] GUPTA, M., RELAN, B., YADAV, R., et al. Wideband digital integrators and differentiators designed using particle swarm optimisation. *IET Signal Processing*, 2014, vol. 8, no. 6, p. 668–679. DOI: 10.1049/iet-spr.2013.0011
- [19] JALLOUL, M. K., AL-ALAOUI, M. A. Design of recursive digital integrators and differentiators using particle swarm optimization. *International Journal of Circuit Theory and Applications*, Jul. 2015, vol. 44, no. 5, p. 948–967. DOI: 10.1002/cta.2115
- [20] UPADHYAY, D. K. Recursive wideband linear phase digital differentiators and integrators. In *Proceedings of the International Conference* on Computing Communication Control and Automation (ICCUBEA). 2015, p. 927–931. DOI: 10.1109/ICCUBEA.2015.184
- [21] AGGARWAL, A., RAWAT, T. K., KUMAR, M., et al. Efficient design of digital FIR differentiator using L₁-method. *Radioengineering*, Jun. 2016, vol. 25, no. 2, p. 383–389. DOI: 10.13164/re.2016.0383
- [22] AL-ALAOUI, M. A. Using fractional delay to control the magnitude and phases of integrators and differentiators. *IET Signal Processing*, Jun. 2007, vol. 1, no. 2, p. 107–119. DOI: 10.1049/iet-spr:20060246

About the Authors ...

Kriti GARG received her Bachelor of Engineering in Electronics and Communication Engineering from Panjab University, Chandigarh in 2014 and Master of Technology in Signal Processing from Netaji Subhas Institute of Technology, University of Delhi, New Delhi in 2016. She is currently working as Design Engineer at NXP Semiconductors, Noida. Her research interests include Digital Signal Processing, Signal System, Circuit System and Digital Design.

Dharmendra K. UPADHYAY holds Bachelor of Engineering in Electronics and Communication Engineering from Kumaon University, Nainital and Master of Technology in Communication and Information Systems from Aligarh Muslim University, Aligarh, U.P. He obtained Ph.D. from Uttarakhand Technical University, Dehradun, Uttarakhand. He is currently working as Professor in Division of Electronics and Communication Engineering, Netaji Subhas Institute of Technology, University of Delhi, New Delhi. His research interests include Digital Signal Processing, Microwave Filter and Antenna Designs. He has published several papers in various International Journals including IET, Electronics Letters. He is a life member of ISTE. He is also the reviewer of several reputed International Journals namely IEEE Transactions on Circuits and Systems-I; Circuits and Systems-II; Signal Processing; Circuits Systems and Signal Processing-Springer, ETRI Journal etc.