

Novel Design of Recursive Differentiator Based on Lattice Wave Digital Filter

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Abstract. *In this paper, a novel design of third and fifth order differentiator based on lattice wave digital filter (LWDF), established on optimizing L_1 -error approximation function using cuckoo search algorithm (CSA) is proposed. We present a novel realization of minimum multiplier differentiator using LWD structure leading to requirement of optimizing only N coefficients for N th order differentiator. The γ coefficients of lattice wave digital differentiator (LWDD) are computed by minimizing the L_1 -norm fitness function leading to a flat response. The superiority of the proposed LWDD is evident by comparing it with other differentiators mentioned in the literature. The magnitude response of the designed LWDD is found to be of high accuracy with flat response in a wide frequency range. The simulation and statistical results validates that the designed minimum multiplier LWDD circumvents the existing one in terms of minimum absolute magnitude error, mean relative error (dB) and efficient structural realization, thereby making the proposed LWDD a promising approach to digital differentiator design.*

Keywords

Lattice wave digital filter, digital differentiator, wide-band, L_1 -CSA, minimum multiplier

1. Introduction

In recent years, digital differentiators are widely applied in various fields of signal processing, image processing, biomedical engineering, radar engineering, control systems, etc. [1–4]. The design and realization of digital differentiator have emerged as an active area of research due to its wide range of applications. Digital differentiators are effectively used to compute the time-derivative of real time and/or stored signal, which necessitates their results to be of high accuracy and its structural realization to be robust. The frequency response of an ideal digital differentiator is given by

$$H_d(\omega) = j\omega \quad (1)$$

where $j = \sqrt{-1}$ and $\omega \in [0, \pi]$ is the normalized frequency. An ideal differentiator has a constant phase response of $\frac{\pi}{2}$ (≈ 1.57 rad) over the entire Nyquist frequency range. In available literature, several methods have been extensively explored for designing and implementing a digital differentiator. Interpolation and approximation based techniques [5–8] and different optimization techniques are most prevalent approaches used for the designing purpose [9–16]. The optimization based design of digital differentiator can be described as an approximation problem which comprises of four steps. First, a desired ideal frequency response of the digital differentiator is defined. Second, selection of type of system (either FIR or IIR). Third, developing an optimality criterion to approximate the ideal response. Lastly, an application of an optimization method to compute the optimal system coefficients.

In this paper, instead of a direct form of FIR or IIR system, a new and improved class of IIR system is reported due to design constraints of IIR system. IIR systems suffer from stability problem due to its recursive nature, especially when quantization of signal and coefficient is applied. IIR systems also have limitations like sensitivity to wordlength and coefficient round-off errors which make their implementation tricky. Direct form structure of IIR system based N th order digital differentiator requires $2N + 1$ multiplier coefficients. Due to involvement of large number of multiplications in the filter algorithm, hardware implementation incurs excessive area, delays and power consumption. Analyzing all these limitations and considering the requirement of competent design of the digital differentiator, mathematical modelling based on the lattice wave digital filter system is proposed. The lattice wave digital filter is a specific class of wave digital filter [17]. LWDF structures flaunt many fascinating properties such as low coefficient sensitivity and consequently the low accuracy requirements for register wordlength, higher dynamic range, higher overflow level, lower round-off noise, assurance of stability and good nonlinear properties under finite arithmetic conditions where the effects of rounding, truncation and overflow are present [17–19]. In the past, LWDF structures are used for realizing lowpass-highpass filter, bandpass-bandstop filter and Hilbert transformers [20–22]. Their resulting struc-

tures are highly modular, less sensitive and found to have minimum hardware which make them suitable for signal processors and VLSI implementation.

In the literature, many researchers have proposed different wideband recursive differentiators by approximating the ideal differentiator response efficiently in a full Nyquist frequency range. The use of evolutionary algorithms in optimization of digital operators is an exponentially increasing field with usage of different optimization algorithms for obtaining improved models of digital differentiator. These intelligent algorithms are capable of providing optimal results by minimizing any multi-modal error objective functions in less computation time. Simulated annealing (SA) technique is used to optimize the differentiator designed using segment rule [12]. Genetic algorithm (GA) was practiced for designing of second order recursive differentiator and the designs were shown to perform well in terms of both magnitude and phase response [16]. A third order wideband differentiator using linear programming (LP) technique was introduced in [9]. The techniques SA, GA and Fletcher and Powell optimization were incorporated to optimize the interpolated coefficients of first and higher order differentiator [13]. Furthermore, the modified particle swarm optimization (PSO) algorithm based digital differentiator design was presented and compared with the designs of GA, variants of PSO and PSO-GA hybrid techniques [14]. Jain et al. proposed IIR differentiators using minimax and pole, zero, and constant optimization (MPZCO) methods [15]. The objective of these algorithms are to minimize the mean square error approximation by searching for an optimized set of numerator and denominator coefficients of IIR system. Direct form structure of N th order IIR differentiator requires $2N + 1$ multiplier coefficients. It complicates the search as large computational complexity occur due to the increased number of variable to be optimized and the constraints that need to be incorporated to ensure stability of an IIR system. On the contrary, LWDF is completely characterized by a set of coefficients (γ) that have excellent dynamic range and low wordlength requirements and only N number of γ coefficients are to be optimized for the design of N th order differentiator.

In this paper, CSA technique aims to find the optimal coefficients for LWDD that closely match the ideal counterpart by minimizing the fitness function, that is, L_1 -norm error. The L_1 -norm based optimality criterion is employed due to its ability to produce the flattest response over wide range of frequency that approaches the ideal one [23]. Thus, in order to design digital differentiator with desired response, the L_1 -norm is hybridized with the evolutionary algorithm to find the system coefficients.

In this paper, novelty lies in the fact that the wideband differentiator designed using LWDF system will incorporate all the advantageous properties of LWDF with utilization of minimum hardware and also competent results are gained by using optimization techniques. The success of this combination is validated by the results and comparisons performed in this work. The capability of global search and optimal

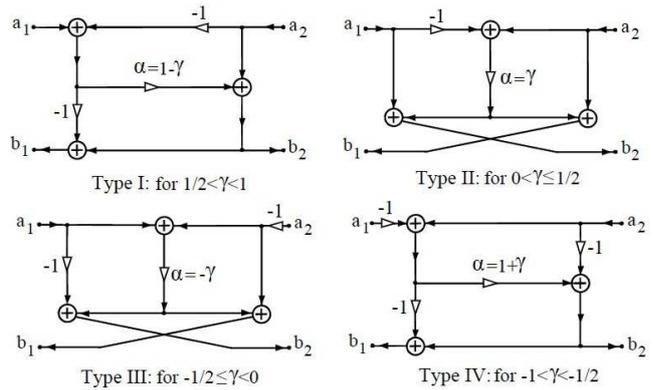


Fig. 1. Signal flow graph of four symmetric two-port adaptors.

robust solution finding feature of cuckoo search algorithm is explored for the design of lattice wave digital differentiator. Thus, the hybrid L_1 -CSA is formulated for designing of the LWDD in order to optimize γ coefficients of a LWDF transfer function. The novel structural realization of the proposed 3rd and 5th order lattice wave digital differentiator is also introduced in this paper. The graphical analysis of magnitude response, phase response, magnitude error response and statistical analysis of absolute magnitude error (AME), mean relative error (MRE) in dB and mean phase error (MPE), demonstrates that the proposed minimum multiplier LWDD outperforms the reported differentiators.

The rest of the paper is segmented as follows. Brief overview of the LWDF along with the mathematical articulation is provided in Sec. 2. Section 3 focuses on the formulation of lattice wave differentiator design problem using L_1 -error function. Section 4 provides the brief overview of the employed optimization algorithm. Simulation results and comparative analysis of the proposed LWDD is provided in Sec. 5 and Sec. 6. Section 7 highlights conclusion of the presented work.

2. Lattice Wave Digital Filter

This section gives a brief overview of lattice wave digital filter structure. Alike WDF, LWDFs are also related to certain analog prototype networks, i.e., lattice networks [17]. LWDF is represented by two parallel branches, which realize all-pass filters. These all-pass filters can be realized by using first- and/or second-order wave digital all-pass structures. These all-pass structures are implemented by using symmetric two-port adaptors and delay elements [18]. An adaptor requires a single multiplication and three additions. The application of adaptor produces an efficient realization in terms of number of multipliers for a given order and have low sensitivity to coefficient quantization. LWDF uses four types of adaptors as its building blocks. The signal flow graphs of four single multiplier symmetric two-port configurations are shown in Fig. 1.

According to [19], the adaptor coefficients γ can be guaranteed to fall in the interval $-1 < \gamma < 1$. Methods to easily calculate these coefficients from the design specifica-

tions have been discussed in [19]. The choice of an adaptor structure depends on the value of γ coefficient. The α/γ conversion expressions is shown in Fig. 1. The value α coefficient should always be positive and less than or equal to half i.e., $0 < \alpha \leq \frac{1}{2}$.

3. Problem Formulation

The ideal differentiator in (1) is approximated to the LWDF system. The transfer function of LWDF system is specified as

$$H_{LWDF}(z) = \frac{1}{a}[H_1(z) - H_2(z)] \quad (2)$$

where $H_1(z)$ and $H_2(z)$ are allpass filter of order P and Q , respectively. In case of lowpass/highpass filters, $P = Q - 1$ or $P = Q + 1$. The overall order of $H_{LWDF}(z)$ is $P + Q$. $H_1(z)$ and $H_2(z)$ can be expressed in terms of adaptor coefficients as

$$H_1(z) = \frac{-\gamma_0 + z^{-1}}{1 - \gamma_0 z^{-1}} \prod_{l=1}^m \frac{-\gamma_{2l-1} + \gamma_{2l}(\gamma_{2l-1} - 1)z^{-1} + z^{-2}}{1 + \gamma_{2l}(\gamma_{2l-1} - 1)z^{-1} - \gamma_{2l-1}z^{-2}}, \quad (3)$$

$$H_2(z) = \prod_{l=m+1}^{m+n} \frac{-\gamma_{2l-1} + \gamma_{2l}(\gamma_{2l-1} - 1)z^{-1} + z^{-2}}{1 + \gamma_{2l}(\gamma_{2l-1} - 1)z^{-1} - \gamma_{2l-1}z^{-2}} \quad (4)$$

where $m = \frac{P-1}{2}$ and $n = \frac{Q}{2}$ and γ is the adaptor coefficient that characterizes LWDF.

In this paper, design of 3rd and 5th order differentiator is considered. The differentiator of even order can also be realized using LWDF, however, in that case complex coefficients are required. In this paper, only real coefficients, odd order differentiator design is considered and the transfer function of 3rd and 5th order LWDF system for designing of differentiator is expressed as

$$H_{LWDF}^{3rd}(z) = \frac{1}{a} \left[\left(\frac{-\gamma_0 + z^{-1}}{1 - \gamma_0 z^{-1}} \right) - \left(\frac{-\gamma_1 + \gamma_2(\gamma_1 - 1)z^{-1} + z^{-2}}{1 + \gamma_2(\gamma_1 - 1)z^{-1} - \gamma_1 z^{-2}} \right) \right], \quad (5)$$

$$H_{LWDF}^{5th}(z) = \frac{1}{a} \left[\left(\frac{-\gamma_0 + z^{-1}}{1 - \gamma_0 z^{-1}} \right) \left(\frac{-\gamma_1 + \gamma_2(\gamma_1 - 1)z^{-1} + z^{-2}}{1 + \gamma_2(\gamma_1 - 1)z^{-1} - \gamma_1 z^{-2}} \right) - \left(\frac{-\gamma_3 + \gamma_4(\gamma_3 - 1)z^{-1} + z^{-2}}{1 + \gamma_4(\gamma_3 - 1)z^{-1} - \gamma_3 z^{-2}} \right) \right] \quad (6)$$

where a is the scaling factor. In order to obtain the digital differentiator with the desired specification, an objective function in terms of error between the ideal frequency response and LWDF frequency response is developed. The objective function is formulated using L_1 -norm to obtain a set of optimized coefficients and can be expressed as

$$\|E\|_1 = \sum_{\omega} |e(\omega)| \quad (7)$$

where $\|\cdot\|$ denotes norm of the function and $e(\omega) = H_d(\omega) - H_{LWDF}(\omega)$ is the error objective function. The objective fitness function is minimized iteratively to obtain the optimized γ coefficients of the 3rd and 5th order LWDF (given in (5) and (6), respectively) to design minimum multiplier differentiator with desired specifications. The motivation behind implementing the L_1 -norm fitness function is due to the fact that it is capable of delivering flattest response amongst others, such as the L_2 and L_∞ -norms [23], [24].

4. Brief Outline of Cuckoo Search Algorithm

In this paper, CSA is used to design minimum multiplier digital differentiator. CSA tries to find the optimal γ coefficients by iteratively minimizing the error objective function, leading to development of the lattice wave digital differentiator. In previous literatures, GA and PSO are most utilized optimization algorithm for designing of optimal recursive wideband differentiators. However, when tested on different benchmark functions, CSA found to be potentially more powerful than the GA and PSO [25].

Cuckoo search algorithm is instigated by the unique breeding behavior of cuckoo bird and the concept of Lévy flights which are observed in some species of birds and animals [25–27]. Cuckoo birds depend on some other bird’s nest for hatching their eggs. Cuckoo attempts to determine a nest where the host bird has recently laid eggs. Cuckoo uses this nest to hide its own eggs. If the host bird identifies that the eggs are not its own, it may abandon the nest or either choose to destroy the alien eggs. This heads to the evolution of the cuckoo eggs, which make attempt to mimic the eggs of the host bird.

To apply cuckoo search algorithm, following assumptions are considered:

- (i) Cuckoo bird hatches one egg at a time and hide it in the host’s nest chosen randomly.
- (ii) The nests with the best host environment and with the best eggs will survive and move forward to the next generations.
- (iii) The number of host nests is fixed.
- (iv) The probability of identifying alien eggs by the host bird is P_a .

The Lévy flights are the forward steps taken by living beings, such as birds, insects and animals in search of their food. Lévy flight refers to a series of straight line flights followed by sudden 90° turns. The usage of Lévy flight for choosing a random new nest is the main factor for improving the performance of CSA. To get a new random nest following Lévy flight based formula is used,

$$x_l = x_i + v \oplus Lévy(\lambda) \quad (8)$$

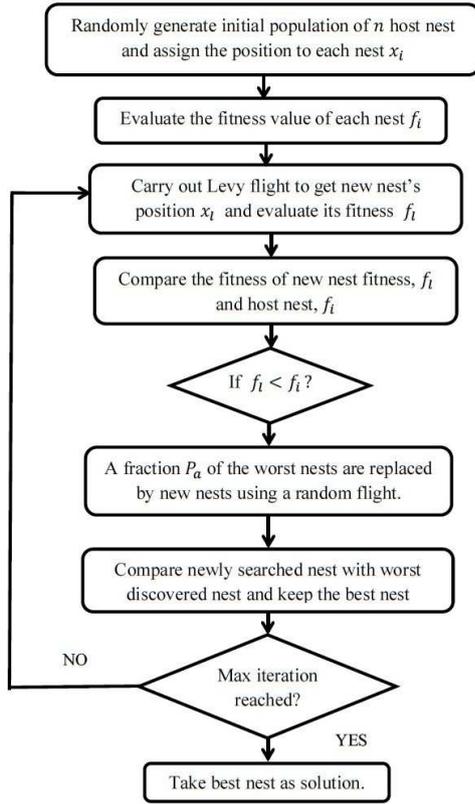


Fig. 2. Flow chart of Cuckoo search algorithm.

where ν is the step size related to the problem specified, \oplus represents entry wise multiplication and λ is a Lévy flight parameter. This equation represents a random walk which is a Markov chain which means its next step depends on the current location and the transition probability. The implementation steps of CSA are explained with a flow chart presented in Fig. 2.

5. Simulation Analysis

This section shows the design process of lattice wave digital differentiator using CSA and its efficient LWD structural realization with minimum multipliers. To incur the proposed LWDD, simulations are performed in MATLAB on Intel Core i5, 3.20 GHz with 2 GB RAM. The optimal set of CSA parameters for design of minimum multiplier differentiator are reported in Tab. 1. After exhaustive simulation and analysis, the best optimal coefficients for the designed 3rd and 5th order LWDD are calculated by minimizing error fitness function using L_1 -CSA are reported in Tab. 2. Best results are reported here after 100 simulation trails with random parameter value selection. The tuning of controlling parameters is a typical task and there exists no definite methodology in the available literature to provide an optimal set of parameter values. So, optimal parameter values can differ for different problems.

Parameters	CSA
Population size	25
Maximum iterations	300
Tolerance	10^{-5}
Lower bound (X_{min})	-1
Upper bound (X_{max})	1
Initial value of all coefficients	0.01
Discovering rate of alien eggs (P_a)	0.25

Tab. 1. Control Parameters of CSA for the optimized LWDD.

Order	γ coefficients	γ range	Adaptor type	α coefficients
3rd	$\gamma_0 = -0.41434$	$-\frac{1}{2} \leq \gamma < 0$	III	$\alpha_0 = 0.41434$
	$\gamma_1 = -0.06457$	$-\frac{1}{2} \leq \gamma < 0$	III	$\alpha_1 = 0.06457$
	$\gamma_2 = -0.64613$	$-1 < \gamma < -\frac{1}{2}$	IV	$\alpha_2 = 0.35387$
	$a = 0.65775$			
5th	$\gamma_0 = -0.08045$	$-\frac{1}{2} \leq \gamma < 0$	III	$\alpha_0 = 0.08045$
	$\gamma_1 = -0.33818$	$-\frac{1}{2} \leq \gamma < 0$	III	$\alpha_1 = 0.33818$
	$\gamma_2 = -0.91047$	$-1 < \gamma < -\frac{1}{2}$	IV	$\alpha_2 = 0.08953$
	$\gamma_3 = -0.20044$	$-\frac{1}{2} \leq \gamma < 0$	III	$\alpha_3 = 0.20044$
	$\gamma_4 = -0.83884$	$-1 < \gamma < -\frac{1}{2}$	IV	$\alpha_4 = 0.16116$
	$a = 0.64531$			

Tab. 2. Optimized Coefficients for the 3rd and 5th order LWDD using L_1 -CSA.

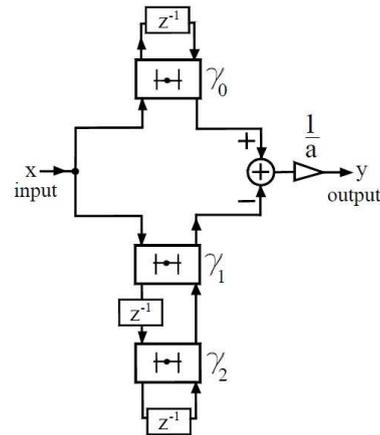


Fig. 3. Structural realization of the 3rd order lattice wave digital differentiator.

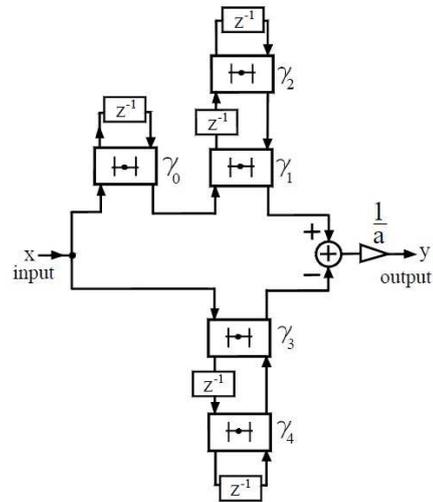


Fig. 4. Structural realization of the 5th order lattice wave digital differentiator.

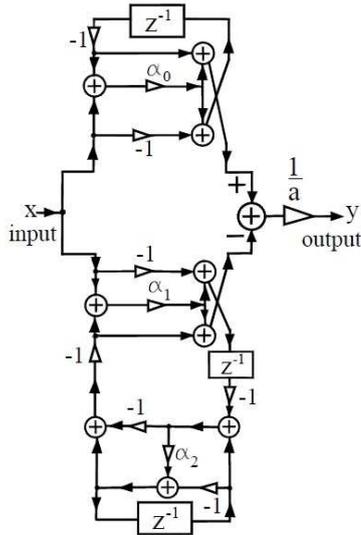


Fig. 5. Signal flow diagram of the 3rd order lattice wave digital differentiator.

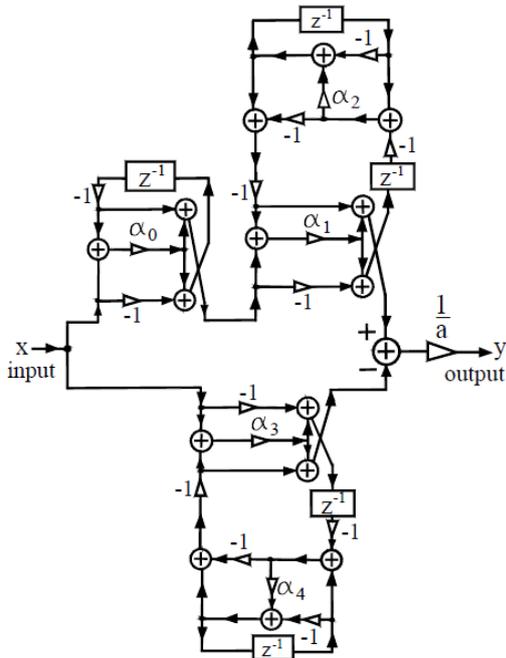


Fig. 6. Signal flow diagram of the 5th order lattice wave digital differentiator.

Table 2 also provides information about the adaptor type chosen depending on the value of γ coefficients and the exact value of adaptor multiplier coefficients, α . The lattice wave digital realization of the designed 3rd order LWDD is shown in Fig. 3. A minimum multiplier design for the 3rd order LWDD is procured where only three adaptors are utilized, where each adaptor comprises of a single multiplier. The lattice wave digital realization of the designed 5th order LWDD is shown in Fig. 4.

A minimum multiplier design for the 5th order LWDD is obtained, thereby reducing the computational requirement or the complexity of hardware in implementation. The signal flow diagram of 3rd and 5th order lattice wave digital differentiator is depicted in Figures 5 and 6, respectively. The

absolute magnitude error of 1.4706 and 0.3243 have been achieved for the 3rd and 5th order LWDD optimized using L_1 -CSA. The execution time to achieve corresponding values of AME and MPE is 80.4564 second and 106.2131 second, respectively. Furthermore, statistical analysis shows that the mean relative error (dB) of -51.7828 and -64.0481 have been achieved for the 3rd and 5th order LWDD optimized using CSA. The MRE is computed over the complete frequency range using following equation

$$MRE = \frac{1}{N} \sum_{i=1}^N \left| \frac{H_d(\omega) - H_{LWDF}(\omega)}{H_d(\omega)} \right|, \quad 0 \leq \omega \leq \pi. \quad (9)$$

6. Comparison of the Proposed Differentiators with the Existing Ones.

To verify and evaluate the efficiency, the designed LWDD is compared with the existing differentiators. Several existing differentiators procured through different methods are tabulated in Tab. 3 for comparison.

The comparison is performed on the basis of procured magnitude response, magnitude error response and phase curve. The magnitude response plots of 3rd and 5th order LWDDs are obtained by simulating (5)–(6), after substituting the value of obtained optimal γ coefficients. Figures 7 and 8, represent the graphical comparison of magnitude and phase response of the 3rd order LWDD and all the existing 3rd order differentiators mentioned in Tab. 3, along with the ideal response. The magnitude error plot of the designed and reported 3rd order differentiators is shown in Fig. 9.

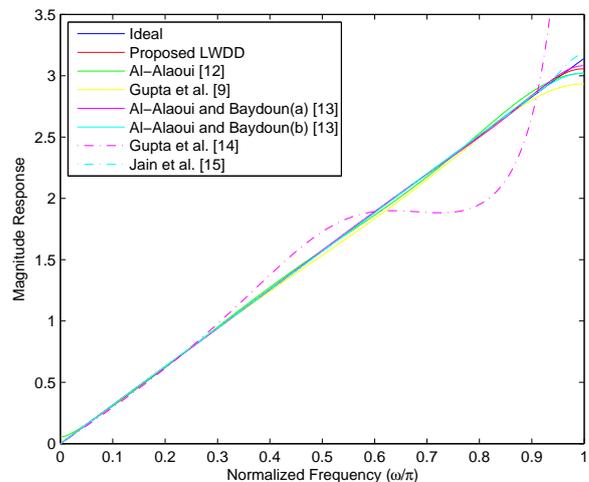


Fig. 7. Magnitude response comparison of the proposed 3rd order LWDD and existing differentiators.

Order	Reference	Method	Numerator coefficient	Denominator coefficient
3rd	Proposed LWDD	CSA	0.53177; 0.8672; -0.8672; -0.53177	1.0000; 1.1022; 0.34957; 0.02675
	Al-Alaoui	3-segment	0.01903; -0.02905; 1.1230; -1.1810	1.0000; 0.1846; -0.001748; 0.03484
	Gupta et al.	LP	1.0000; -1.0000; 0.0000; 0.0000	0.3290; 0.8077; -0.1694; 0.0338
	Al-Alaoui and Baydoun (a)	GA	1.1533; -0.4432; -0.7060; -0.0041	1.0000; 0.7981; 0.0884; 0.0000
	Al-Alaoui and Baydoun (b)	SA	1.1555; -0.3580; -0.7140; -0.0833	1.0000; 0.8662; 0.1612; 0.0028
	Gupta et al.	PSO	0.3237; 1.0000; -0.7133; -0.6124	1.0000; 1.0000; 0.2759; 0.1595
	Jain et al.	MPZCO	1.0000; -0.0243; -0.8315; -0.1436	0.8646; 1.0001; 0.2470; 0.0056
5th	Proposed LWDD	CSA	0.1295; 0.6828; 0.6019; -0.6019; -0.6828; -0.1295	0.6541; 1.3175; 0.9691; 0.3156; 0.0435; 0.0018
	Devate et al.	curve fitting	0.2500; 2.1408; 2.0522; -2.0523; -2.1407; -0.2499	2.2257; 4.1683; 2.6300; 0.6353; 0.0517; 0.0000

Tab. 3. Optimal coefficients of the existing digital differentiators using different design methods.

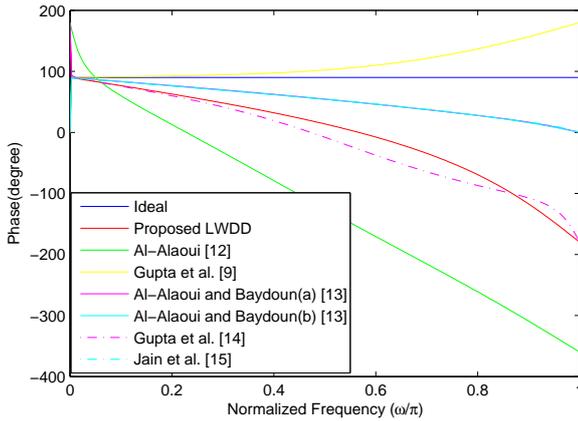


Fig. 8. Phase response comparison of the proposed 3rd order LWDD and existing differentiators.

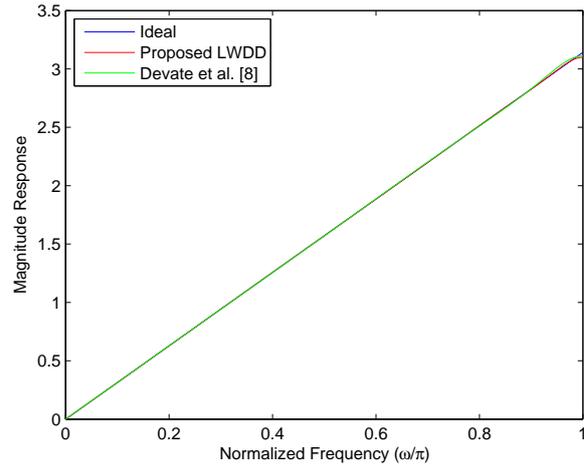


Fig. 10. Magnitude response comparison of the proposed 5th order LWDD and existing differentiator.

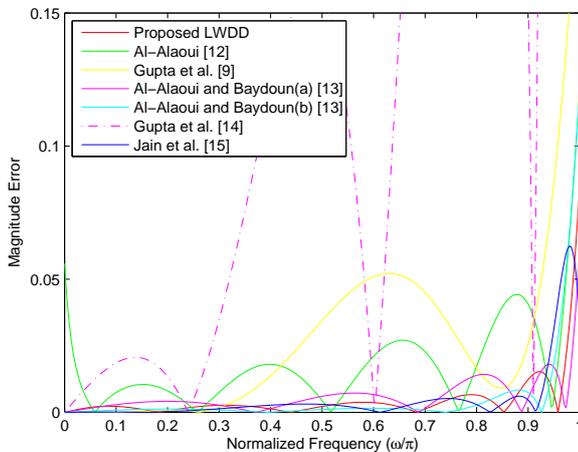


Fig. 9. Magnitude error comparison of the proposed 3rd order LWDD and existing differentiators.

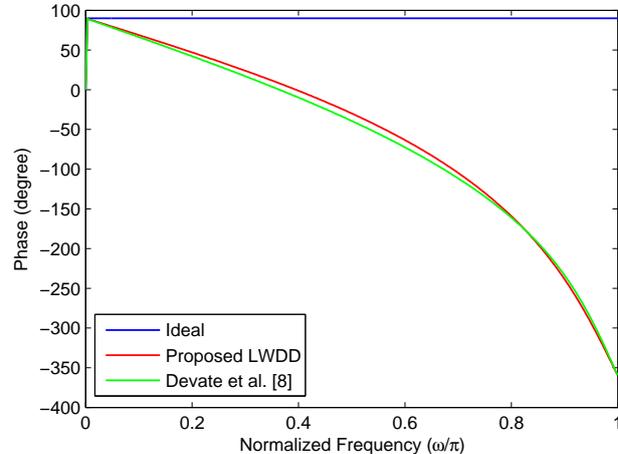


Fig. 11. Phase response comparison of the proposed 5th order LWDD and existing differentiator.

It is discernible that the deviation of magnitude from ideality varies least in case of the proposed LWDD over a wide range of frequency. Figures 10 and 11, represent the graphical comparison of magnitude response and phase response of the 5th order LWDD and the existing 5th order differentiator mentioned in Tab. 3. Further in Fig. 12, the magnitude error plot of the designed and existing 5th order differentiator is shown. All reported figures depict that the proposed minimum multiplier LWDD approximates the magnitude response of the ideal differentiator over wide frequency band in comparison to the reported differentiators.

For the affirmative analysis, absolute magnitude error, mean relative error (dB) and number of multiplier operations are the main parameters taken into consideration to evaluate performance of the designed LWDDs. The value of AME, MRE (dB) and MPE of the proposed minimum multiplier 3rd and 5th order LWDD and different reported differentiators are provided in Tab. 4. The absolute magnitude error is observed to be the lowest (1.4706 and 0.3243, respectively) for the proposed 3rd and 5th order LWDD amongst all reported differentiators. The MRE and mean phase error are evaluated over the complete frequency range. The MPE (rad) is calculated using the following equation

Order	Reference	Absolute magnitude error	Mean relative error (dB)	Mean phase error (rad)
3rd	Proposed LWDD	1.4706	-51.7828	1.4896
	Al-Alaoui [12]	5.5590	-13.3765	3.7851
	Gupta et al. [9]	8.4601	-38.0100	0.4117
	Al-Alaoui and Baydoun (a) [13]	1.8570	-46.9583	0.6726
	Al-Alaoui and Baydoun (b) [13]	1.5120	-52.7418	0.6682
	Gupta et al. [14]	91.4645	-17.8199	1.8253
	Jain et al. [15]	1.5372	-52.9934	0.6632
5th	Proposed LWDD	0.3243	-64.0481	2.5895
	Devate et al. [8]	0.9008	-57.4759	2.6660

Tab. 4. Statistical comparison of the designed LWDD with existing designs.

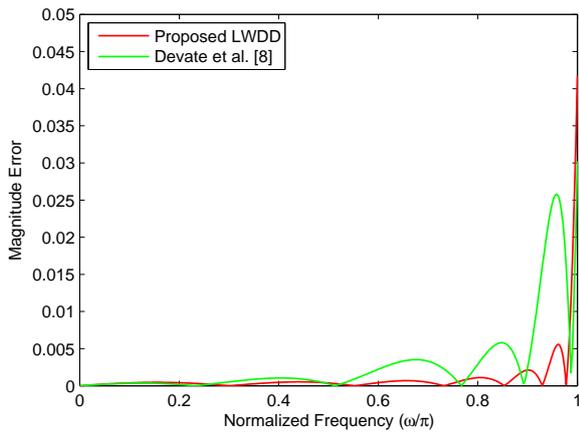


Fig. 12. Magnitude error comparison of the proposed 5th order LWDD and existing differentiator.

$$MPE = \frac{1}{N} \sum_{i=1}^N \left| \frac{\angle H_d(\omega) - \angle H_{LWDF}(\omega)}{\angle H_d(\omega)} \right|, \quad 0 \leq \omega \leq \pi. \tag{10}$$

Comparative results reveal that the 3rd and 5th order differentiators designed using LWDF possess the lowest MRE (dB) (-51.7828 dB and -64.0481 dB, respectively) amongst all. However, MPE of the proposed 3rd order LWDD is slight higher than that of differentiator designed using GA and MPZCO [13], [15]. Whereas, MPE of the proposed 5th order LWDD is lower than the differentiator proposed by Devate et al. [8]. On the grounds of the above facts, it is inferred that the proposed minimum multiplier wideband LWDD performs better than the reported differentiators by approaching the ideal curve to the maximum and also inherit all the advantageous properties of LWDF.

Transfer function of a recursive differentiator can be realized using different structures. For infinite precision arithmetic all structures produce the same output for a given input, but under finite precision arithmetic different kinds of errors occur. The best structure is a trade-off between many different aspects such as stability, coefficient sensitivity and other quantization effects. Hence, need of the competent structures for differentiation applications is rectified by using lattice wave digital structural realization. Estimation of hardware requirements of the proposed LWDD structure

Order	Differentiator designs	Number of Multipliers
3rd	Proposed LWDD	4
	Al-Alaoui	7
	Gupta et al.	5
	Al-Alaoui and Baydoun(a)	6
	Al-Alaoui and Baydoun(b)	7
	Gupta et al.	5
5th	Jain et al.	7
	Proposed LWDD	6
	Devate et al.	11

Tab. 5. Multiplication operations required by the proposed LWDD and the existing differentiators.

can be done by counting the number of multiplications required for its implementation. The number of multiplication operations required by the proposed 3rd and 5th order differentiators are computed from the signal-flow graph provided in Figures 5 and 6, respectively. Arithmetic complexity of the reported digital differentiators is computed by considering their realization using direct form II structure.

The number of multiplication operations required by the proposed LWDD and the existing differentiators are provided in Tab. 5, which are computed from the signal-flow graph. The number of multiplication operations required by the proposed N th order LWDD is $N + 1$ multiplication, whereas, the exiting N th order differentiator structures require $2N + 1$ multiplication. The proposed 3rd and 5th order LWDD require 43% and 45% less multipliers as compared to the reported 3rd and 5th order differentiators. This implies that the LWDD yields a smaller arithmetic complexity, in terms of multiplications, compared to the existing ones leading to minimum hardware cost and making it suitable for VLSI implementation. Based on the observations, it is ratified that the proposed minimum multiplier LWDD outperforms all the existing differentiators both in design as well as structural realization.

7. Conclusion

A novel method for designing of minimum multiplier digital differentiator based on lattice wave digital filter and L_1 -CSA is proposed. The optimal coefficients of 3rd and 5th

order lattice wave digital differentiator are procured by minimizing the L_1 -error objective function by means of cuckoo search algorithm. The proposed LWDDs accurately approximate the ideal response with very small AME and MRE over the entire frequency range. Simulation results clearly demonstrate the effective performance of the proposed differentiator over other existing differentiators along with reduced computational complexity. A significant improvement in hardware utilization is achieved by incorporating the lattice wave digital structural realization which makes the proposed LWDD useful in various applications.

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