

Analysis of Access Delay and Delay Jitter for the CSMA/CA Mechanism with Inactive Period

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Abstract. *Owing to the distributed features and flexibility, carrier sense multiple access with collision avoidance (CSMA/CA) mechanisms have been widely adopted in wireless networks. Considering energy consumption, we devote to exploring an energy efficient carrier sense multiple access with collision avoidance (EE-CSMA/CA) mechanism. The access delay and delay jitter are two of most important quality of service (QoS) metrics. EE-CSMA/CA inevitably experiences the tortures bred from its delay and delay jitter performance, because its frame structure with inactive period brings delay. In this paper, we aim to analyze the access delay and delay jitter of packets for the CSMA/CA with inactive period under the unsaturated conditions. Based on the Markov theory, we model the mechanism of EE-CSMA/CA as a four-dimensional Markov chain, and the fourth variable of Markov chain differentiates the active period and the inactive period of EE-CSMA/CA. Then, deriving in probability generation domain, we get the probability distribution of service time and obtain the access delay. Finally, the queue in a node is decoupled, and two queues with different states of empty and non-empty are configured logically, which helps to enable the complex analysis of delay jitter. Based on the whole probability analysis, the probability distribution of delay jitter is obtained. We show that the active ratio (active time over whole frame time) affects the access delay and delay jitter. When the active ratio is constant, the duration of active period has less effect on the access delay while it deteriorates delay jitter performance.*

Keywords

CSMA/CA mechanism, inactive period, unsaturated conditions, Markov chain, access delay, delay jitter

1. Introduction

The fifth generation (5G) wireless networks are expected to support various services such as Voice over Internet Protocol (VoIP), real-time video streams [1], interactive online games and so on. Different applications have different quality of service (QoS) requirements. Real-time applications have requirements for delay and jitter of delay,

for example, VoIP and Skype. The authors of [2] analyzed the delay of VoIP networks, and the authors of [3] stated that the Skype required to satisfy the QoS requirements in terms of not only delay but also delay jitter. It was reported that suffering from jittered voice, a lot of users hung up their calls due to the unintelligible voice conversation [3]. The analysis of the delay and the delay jitter is the essential preliminary step for the resource allocation of networks. Owing to the distributed features and flexibility, carrier sense multiple access with collision avoidance (CSMA/CA) mechanisms have been widely adopted in wireless networks. For the sake of energy efficiency, some of CSMA/CA mechanisms introduce the inactive period in the frame structure (such as protocols in IEEE 802.15 series). Nodes sleep and don't transmit packets during the inactive period. The introduction of inactive period can also improve the spectral efficiency of coexisting networks [4]. However, inactive periods inevitably incur the delay and delay jitter. The first step to analyze the delay and delay jitter of CSMA/CA mechanism is to model the service process. Due to the stochastic nature of the CSMA/CA mechanism, it is difficult to analyze the service process and the service time (access delay) of a packet. The delay jitter is involved with delay. The delay of every packet is random due to the collision, which complicates the analysis of delay jitter. We consider the CSMA/CA mechanism with inactive period, which increases another difficulty to the analysis of delay and delay jitter.

Several works adopted the queuing theory to analyze the average delay of packets. In these works, the service time was assumed to be a statistical process, such as the Markov process [5], independent and identical distributed (IID) process following exponential distribution [6] or general distribution [7]. Some works took the characteristics of the specific service mechanisms of networks into consideration. Inspired by Bianchi's seminal Markov model of CSMA/CA mechanism [8], the authors of [9] and [10] dealt with the average access delay analysis of the CSMA/CA mechanisms in saturation traffic scenarios. Considering the unsaturated traffic, the authors of [11] put forward an extended Markov model which added a state to abstract unsaturated situations, and analyzed the average access delay performance. The authors of [12] analyzed the delay of user datagram protocol (UDP) traffic jointly con-

sidering the transmission control protocol (TCP) congestion control mechanisms. In recent years, energy issues have raised concerns. Some experts begin to explore the protocols with inactive period. The authors of [13] evaluated the average access delay performance of single-hop IEEE 802.15.4 networks.

Jitter of delay is another important QoS metric for real-time services, which is defined as the difference between the values of delay of two successive packets [14]. In the last decade, some studies have been devoted to estimating of delay jitter by the steady-state analysis method. Due to the complexity of the practical service mechanism, most of these works have been done in the context that the service time was abstracted as a certain distribution, for example, exponential distribution [15], [16] and deterministic distribution [17], [18]. Our team analyzed the average delay jitter for the tandem queuing system model, where the service time was assumed to be geometrically distributed [19]. However, the average delay jitter is one of features of the delay jitter, which cannot depict the delay jitter vividly. Hence, a transient analysis method for the delay jitter of packets was proposed in [20], where the service time was modeled as an IID process following exponential distribution. In our recent work [21], delay jitter of packets was estimated by transient analysis method in wired internet protocol (IP) concatenated networks, where a packet is served through exactly one slot in IP networks. In all aforementioned works, delay jitter issues of practical networks are not tackled.

In this paper, we propose an energy efficient CSMA with collision avoidance (EE-CSMA/CA) mechanism. In the EE-CSMA/CA mechanism, nodes don't sense the channel state during backoff periods and stay in sleep mode during the inactive period. The service time is the time to serve a packet. The challenge of the service time analysis for CSMA/CA lies in the space-relevance among the service processes of nodes. For the frame with inactive period, how to quantify the effect of frame structure on the service time of EE-CSMA/CA is still an unexploited problem. We define active ratio R as the ratio of the duration of active period to whole frame duration, and investigate the effect of R on access delay as well as delay jitter. On the other hand, the traffic arrival is unsaturated generally. For the EE-CSMA/CA, only the nodes having packets in queues take part in the contention, which changes the relevance of service processes of nodes. Hence, the unsaturated traffic situation makes the space-relevance of service processes of nodes more complicated. The service time of packets is random due to the collisions of packets. The randomness of the arrival and service makes the delay of every packet different. The delay jitter is involved with delay. The random feature of delay makes the analysis of delay jitter intractable. For handling the above issues, we devote to the analysis of the access delay and delay jitter of EE-CSMA/CA in this paper. Our main contributions are summarized as follows.

- For EE-CSMA/CA with inactive period, we model the service process of a packet as a four-dimensional Markov chain, and the fourth variable of Markov chain differentiates the period of the service process being active or inactive. Considering the unsaturated traffic situation, we model the number of packets in the queue of a node as a discrete time Markov chain, and obtain the probability of the queue being empty which affects the idle probability of the channel and the probability of successful transmission. Based on these derived probabilities, we obtain the probability distribution of the service time in probability generation domain.
- Depending on the probability distribution of the service time, we analyze the delay jitter of a packet. In unsaturated traffic conditions, the queue in a node can be decoupled into two states of empty and non-empty, which helps to enable the complex analysis of delay jitter. When the queue of node is empty, the delay jitter of a packet is associated with the service. When the queue of node is non-empty, the delay jitter is decided by the relation between the inter-arrival time of two sequential packets and the service time of the packet. Finally, we obtain the probability distribution of delay jitter relying on the whole probability analysis.
- We study the influence of frame configuration on the access delay and the delay jitter of packets. When the active ratio is constant, the duration of active period has less effect on the access delay while it deteriorates delay jitter. For energy saving and QoS guarantee, it is necessary to set the frame structure reasonably. Under the access delay and delay jitter constraints, we estimate suitable durations of active period (i.e. the maximum durations of inactive period) by the dichotomy, respectively.

This paper is organized as follows. Section 2 describes the system model. In Sec. 3, firstly, we model the EE-CSMA/CA mechanism, which takes both the active period and the inactive period into consideration. Secondly, we derive a probability distribution of the service time and the access delay. Thirdly, we analyze the delay jitter of packets and obtain the probability distribution of delay jitter. Finally, we estimate the minimum duration of the active period constrained by access delay or delay jitter through the dichotomy, respectively. Simulations are presented in Sec. 4. We conclude this paper in Sec. 5. Table 1 shows the notations used in this paper and their descriptions.

2. System Model

In this paper, we consider the unsaturated system which contains M nodes and one access point (AP). Time

Notation	Description
M	The number of nodes
$macMinBE$	The minimum backoff exponent
$macMaxBE$	The maximum backoff exponent
W_i	The contention window size at the i -th backoff stage
m	The maximum value of backoff
n	The maximum value of retransmission
T_1	The duration of active period
T_2	The duration of inactive period
T	The duration of frame structure
R	The ratio of active period duration to frame duration
T_p	The required time to transmit a packet
P_{AI}	The transition probability from the active period to the inactive period
P_{IA}	The transition probability from the inactive period to the active period
P_{II}	The transition probability of staying in inactive period
w	The probability that the node attempts to the first clear channel assessment (CCA)
α	The probability that a channel is sensed busy when a node performs the first CCA
β	The probability that a channel is sensed busy when a node performs the second CCA
λ	Average arrival rate of packets
l_0	The probability of no packets in the queue
D_k	The delay of the k -th packet
D_k^s	The needed time to serve the k -th packet
J_k	The delay jitter of the k -th packet

Tab. 1. Notations and descriptions.

is divided into slots with the equal duration. The length of packets is constant and occupies integer number of slots. The packets can only be transmitted at the beginning of a slot.

The wireless network employs the EE-CSMA/CA for channel access. Figure 1 depicts the frame structure, which includes active period and inactive period. During the active period, nodes adopt CSMA/CA to transmit packets. For the sake of energy efficiency, nodes don't sense the channel during the backoff process, and sense the channel only when the backoff time counter is zero. Nodes stay in sleep mode during the inactive period. During the inactive period, the backoff time counters of all nodes are frozen. Nodes resume their backoff processes in the next active period. In this paper, we focus on analyzing the impact of EE-CSMA/CA mechanism on the access delay and delay jitter. To simplify the problem, we have the assumptions: nodes can hear each other, hence the hidden node problem is avoided; channel is ideal, so packets are error free.

The EE-CSMA/CA mechanism is introduced in Tab. 2. In Tab. 2, i ($0 \leq i \leq m$) denotes the backoff stage, r ($0 \leq r \leq n$) represents the retransmission times. The $macMinBE$ and $macMaxBE$ are the minimum and maximum backoff exponent (BE), respectively. W_0 is the initial backoff window size, where $W_0 = 2^{macMinBE}$.

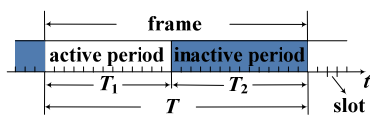


Fig. 1. The frame structure.

EE-CSMA/CA mechanism
1: if the time counter of the active period doesn't reach 0
2: if there is a packet to transmit
3: Initialize the parameters of the CSMA/CA process: $r = 0$;
4: let $i = 0$, $BE = macMinBE$, and choose a random backoff time from $[0, W_0 - 1]$
5: if the backoff time counter reaches 0
6: perform the clear channel assessment (CCA)
7: if two consecutive CCAs are successful
8: transmit the packet
9: else
10: $i = i + 1$, $BE = \min(BE + 1, macMaxBE)$, $W_i = 2^{BE}$;
11: if $i \leq m$
12: choose a random backoff time from $[0, W_i - 1]$;
13: else
14: update queue and return to step 2
15: end if
16: end if
17: else
18: decrease the backoff time counter at every slot of the active period
19: end if
20: if the packet is not transmitted successfully
21: $r = r + 1$
22: if $r \leq n$
23: return to step 4
24: else
25: update queue and return to step 2
26: end if
27: else
28: update queue and return to step 2
29: end if
30: end if
31: else
32: freeze the backoff time counter of all nodes
33: end if

Tab. 2. EE-CSMA/CA mechanism.

3. Performance Analysis

3.1 The Markov Chain Model

We propose a four-dimensional Markov chain to model the EE-CSMA/CA mechanism, where each state is represented by the four variable $\{s(t), b(t), c(t), h(t)\}$, as shown in Fig. 2. Here, $s(t)$ is defined as the stochastic process representing the backoff stage i ($0 \leq i \leq m$) of a given node. When the node accesses the channel unsuccessfully, the backoff stage of node increases. $b(t)$ is the stochastic process representing the backoff time counter j ($-2 \leq j \leq W_i - 1$). The backoff time counter is decreased by 1 during each slot of an active period. When the backoff time counter is decreased to zero, a node senses the channel. The states $j = -1$ and $j = -2$ stand for the first CCA and the second CCA, respectively. Considering the retransmission, $c(t)$ represents the value of retransmission r ($0 \leq r \leq n$). $h(t)$ represents the active or inactive period of the frame structure, where $f = 0$ and $f = -1$ stand for the active period and inactive period, respectively.

In this Markov chain, the one-step transition probabilities are calculated as follows. Based on the EE-CSMA/CA mechanism, when $j \in [2, W_i - 1]$, nodes jump to

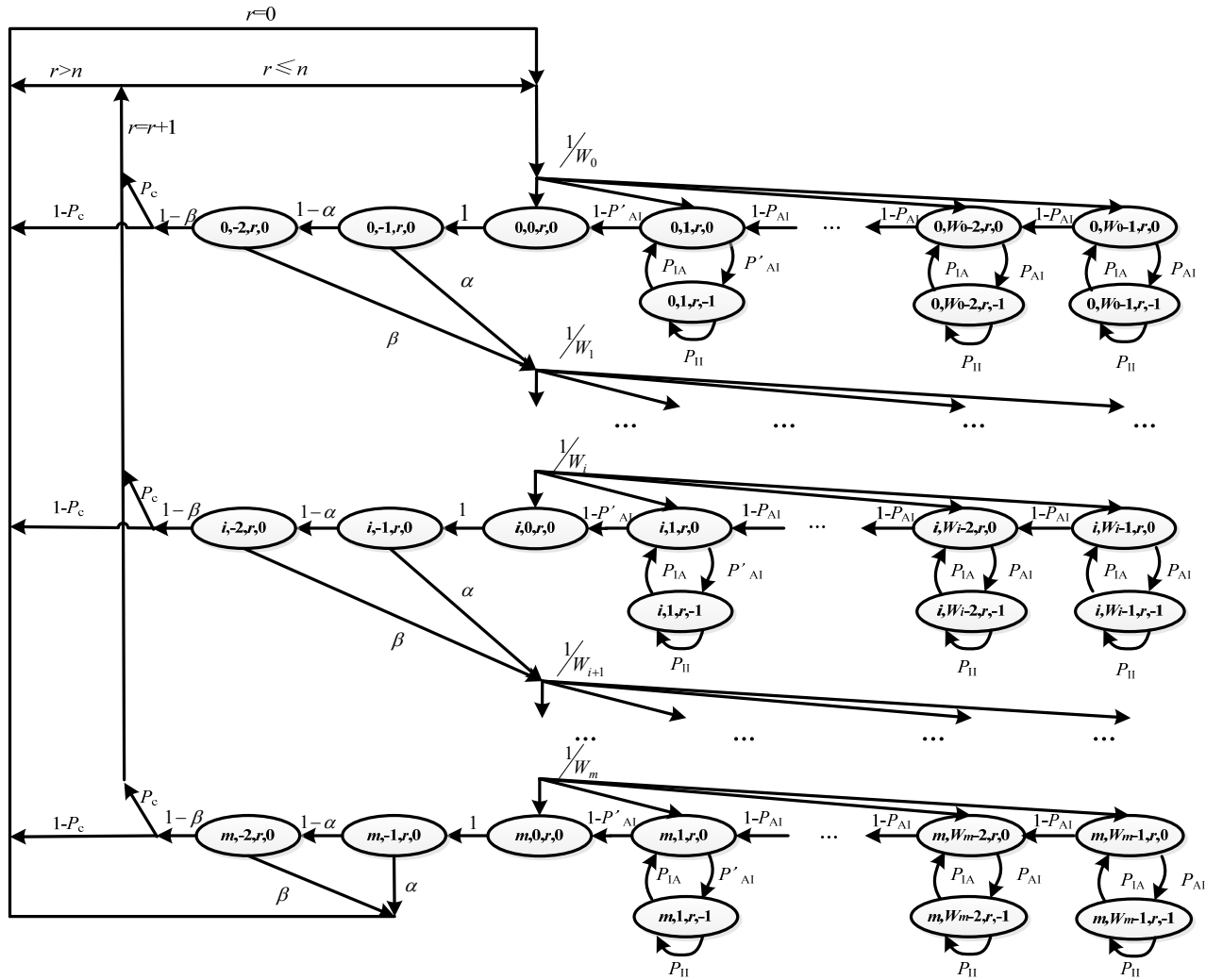


Fig. 2. The four-dimensional Markov chain model.

$$b_{0,0,0,0} = \begin{cases} \left(\frac{1-g^{n+1}}{1-g} \left(\frac{1-x^{m+1}}{1-x} \right) (3-\alpha) + \left(1 + \frac{P'_{AI}}{1-P_{II}} \right) \frac{1}{1-P'_{AI}} \left(\frac{1-x^{m+1}}{1-x} - \frac{1-(x/2)^{m+1}}{1-(x/2)} \frac{1}{W_0} \right) + \left(1 + \frac{P_{AI}}{1-P_{II}} \right) \frac{1}{1-P_{AI}} \left(\frac{W_0}{2} \frac{1-(2x)^{m+1}}{1-2x} - \frac{3}{2} \frac{1-x^{m+1}}{1-x} + \frac{1-(x/2)^{m+1}}{1-(x/2)} \frac{1}{W_0} \right) \right)^{-1} & m \leq s \\ \left(\frac{1-g^{n+1}}{1-g} \left(\frac{1-x^{m+1}}{1-x} \right) (3-\alpha) + \left(1 + \frac{P'_{AI}}{1-P_{II}} \right) \frac{1}{1-P'_{AI}} \left(A - \frac{B}{W_0} + \left(1 - \frac{1}{2^s W_0} \right) C \right) + \left(1 + \frac{P_{AI}}{1-P_{II}} \right) \frac{1}{1-P_{AI}} \left(\frac{W_0}{2} \frac{1-(2x)^{s+1}}{1-2x} - \frac{3}{2} A + \frac{B}{W_0} + \left(\frac{2^s W_0}{2} - \frac{3}{2} + \frac{1}{2^s W_0} \right) C \right) \right)^{-1} & m > s \end{cases} \quad (11)$$

the inactive period if the time counter of the active period reaches zero. However, when $j = 1$, nodes have to avert the packet transmission and jump to the inactive period if the time counter of the active period has reached a value which is lower than the required time to transmit a packet. Hence, the transition probability of achieving inactive period from the active period to is given by

$$P\{i, j, r, -1 | i, j, r, 0\} = \begin{cases} P_{AI} & i \in [0, m], j \in [2, W_i - 1], r \in [0, n] \\ P'_{AI} & i \in [0, m], j = 1, r \in [0, n] \end{cases} \quad (1)$$

where $P_{AI} = 1/T_1$, $P'_{AI} = T_p/T_1$. T_1 and T_2 denote the duration of active period and inactive period, respectively. T_p is the required time to transmit a packet.

Due to the decrement of backoff time counter, the transition probabilities are given by

$$P\{i, j, r, 0 | i, j+1, r, 0\} = \begin{cases} 1 - P_{AI} & i \in [0, m], j \in [1, W_i - 2], r \in [0, n] \\ 1 - P'_{AI} & i \in [0, m], j = 0, r \in [0, n] \end{cases} \quad (2)$$

Let P_{IA} be the transition probability from the inactive period to the active period and P_{II} be the transition probability of staying in the inactive period, then

$$P\{i, j, r, 0 | i, j, r, -1\} = P_{IA} \quad i \in [0, m], j \in [1, W_i - 1], r \in [0, n], \quad (3)$$

$$P\{i, j, r, -1 | i, j, r, -1\} = P_{II} \quad i \in [0, m], j \in [1, W_i - 1], r \in [0, n] \quad (4)$$

where $P_{IA} = T_1/(T_1 + T_2)$ and $P_{II} = 1 - P_{IA}$.

Other transition probabilities of Markov chain are shown in Appendix A.

Let $b_{i,j,r,f}$ be the probability of the steady state (i, j, r, f) of the Markov chain. From Fig. 2, we obtain

$$b_{i,0,r,0} = (\alpha + (1-\alpha)\beta)b_{i-1,0,r,0}, \quad (5)$$

$$b_{0,0,r,0} = (1-\alpha)(1-\beta)P_c \sum_{i=0}^m b_{i,0,r-1,0}. \quad (6)$$

For convenience, let $x = \alpha + (1-\alpha)\beta$ in this paper, then we rewrite $b_{i,0,r,0} = (\alpha + (1-\alpha)\beta)b_{i-1,0,r,0}$ as $b_{i,0,r,0} = x^i b_{0,0,r,0}$. Let $g = P_c(1-x^{m+1})$, so

$$b_{0,0,r,0} = (1-x)P_c \frac{1-x^{m+1}}{1-x} b_{0,0,r-1,0} = g^r b_{0,0,0,0}. \quad (7)$$

Owing to the Markov chain regularities, we have

$$b_{i,j,r,0} = \frac{W_i - j}{W_i} \frac{b_{i,0,r,0}}{1 - P_{AI}} \quad i \in [0, m], j \in [2, W_i - 1], r \in [0, n], \quad (8)$$

$$b_{i,1,r,0} = \frac{W_i - 1}{W_i} \frac{b_{i,0,r,0}}{1 - P'_{AI}} \quad i \in [0, m], j = 1, r \in [0, n]. \quad (9)$$

According to (8) and (9), $b_{i,j,r,f}$ can be expressed as functions of $b_{0,0,0,0}$. The sum of the steady state probabilities of the Markov chain is 1. It can be expressed as

$$\sum_{i=0}^m \sum_{j=0}^{W_i-1} \sum_{r=0}^n b_{i,j,r,0} + \sum_{i=0}^m \sum_{j=1}^{W_i-1} \sum_{r=0}^n b_{i,j,r,0} + \sum_{i=0}^m \sum_{j=2}^{W_i-1} \sum_{r=0}^n b_{i,j,r,0} = 1. \quad (10)$$

Using (5), (6), (8) and (9), equation (10) can be expanded. So $b_{0,0,0,0}$ is given by (11) (below Fig. 2). In (11), $A = (1-x^{s+1})/(1-x)$, $B = (1-(x/2)^{s+1})/(1-(x/2))$, $C = (x^{s+1}-x^{m+1})/(1-x)$ and $s = \text{macMaxBE} - \text{macMinBE}$.

w is defined as the probability that the node attempts to the first CCA in any slot. In other words, w denotes the probability of the backoff time counter of the node being zero in the slot.

$$w = \sum_{i=0}^m \sum_{r=0}^n b_{i,0,r,0} = \frac{1-x^{m+1}}{1-x} \frac{1-g^{n+1}}{1-g} b_{0,0,0,0}. \quad (12)$$

α represents the probability that a channel is sensed busy when the node attempts to the first CCA, that is, at least one of the others is transmitting in the current slot. When a channel is sensed idle for the first CCA, the second CCA will be carried out.

$$\alpha = T_p(1-(1-(1-l_0)w)^{M-1})(1-\alpha)(1-\beta). \quad (13)$$

Let β stands for the probability that a channel is sensed busy when the node performs the second CCA. The channel is sensed busy for the second CCA, that means, at least one of the others has been carried out the second CCA when the node performs the first CCA.

$$\beta = (1-(1-(1-l_0)w)^{M-1})(1-\beta) \quad (14)$$

where l_0 is the probability of no packets in the queue. Referring to [22], we model the buffer of a node as a queuing

system. The number of packets in the queue is characterized by a discrete time Markov chain. Based on the Markov theory, we obtain the steady state probability of the Markov chain in Appendix B. Hence, l_0 is obtained.

By the given equations, we can obtain $w, \alpha, \beta, b_{0,0,0,0}$. Using the above definitions, we get the probability of collision as

$$P_c = (1-(1-(1-l_0)w)^{M-1})(1-\alpha)(1-\beta). \quad (15)$$

The probability of successful transmission is expressed as

$$P_s = 1 - P_c. \quad (16)$$

3.2 The Service Time and the Average Access Delay

A. The Service Time Distribution

In this section, we calculate the service time distribution of packets in probability generation domain. The service time distribution of packets cannot be solved in the probability domain. Since the service time is a non-negative random variable, we analyze the service time relying on our derivation of its probability generation function (PGF). The PGF of the service time of a packet is

$$G(z) = \sum_{D_k^s=0}^{+\infty} p(D_k^s) z^{D_k^s} \quad (17)$$

where $p(D_k^s)$ is the probability that the service time of packet k is D_k^s .

In the backoff process, the backoff time counter either decreases by 1 with the probability $1 - P_{AI}$ or jumps to the inactive period from the active period with the probability P_{AI} . Let $H_d^j(z)$ be the PGF of time interval needed in decreasing the backoff time counter by 1. Then we obtain

$$H_d^j(z) = \begin{cases} P_{AI} z^{T_2} + (1 - P_{AI})z & i \in [0, m], j \in [2, W_i - 1] \\ P'_{AI} z^{T_2} + (1 - P'_{AI})z & i \in [0, m], j = 1 \end{cases} \quad (18)$$

$H_i(z)$ is the PGF of required time that the backoff time counter decreases to zero when the backoff stage is i . $H_i(z)$ is

$$H_i(z) = \begin{cases} \frac{1}{W_i} [1 + H_d^1(z) + H_d^1(z)H_d^2(z) + \dots + \prod_{j=1}^{W_i-1} H_d^j(z)] & i \leq s \\ \frac{1}{W_s} [1 + H_d^1(z) + H_d^1(z)H_d^2(z) + \dots + \prod_{j=1}^{W_i-1} H_d^j(z)] & i > s \end{cases} \quad (19)$$

After the backoff process, the node attempts to access the channel. Within a retransmission, $T(z)$ is the PGF of the required time that the node successfully accesses the channel.

$$T(z) = (1-\alpha)(1-\beta)D^2(z) \sum_{i=0}^m ((\alpha D(z) + (1-\alpha)\beta D^2(z))^i \prod_{q=0}^i H_q(z)) \quad (20)$$

where $D(z)$ is the PGF of the required time (1 slot) for a CCA detection.

$$D(z) = z. \quad (21)$$

Considering the maximum value of retransmission, let $F(z)$ is the PGF of the required time that the node unsuccessfully accesses the channel, and the reason for the failure is that the node has reached the maximum value of backoff within a retransmission.

$$F(z) = (\alpha D(z) + (1-\alpha)\beta D^2(z))^{m+1} \prod_{i=0}^m H_i(z) \quad (22)$$

According to the equations and the EE-CSMA/CA mechanism, $G(z)$ is obtained

$$G(z) = P_s T(z) S(z) \sum_{i=0}^n (T(z)(1-P_s)C(z))^i + (T(z)(1-P_s)C(z))^{n+1} + F(z) \sum_{i=0}^n (T(z)(1-P_s)C(z))^i \quad (23)$$

where $S(z)$ and $C(z)$ are the PGFs of the required time for a successful and unsuccessful transmission of a packet, respectively.

$$S(z) = z^{T_p}, C(z) = z^{T_p}. \quad (24)$$

B. The Average Access Delay

The average access delay is the mathematical expectation of the service time. The probability distribution of the service time is derived by the inverse transform. It is a general distribution and can be expressed as

$$p_{\text{service}}(t) = \sum_{D_k^s=0}^{+\infty} p(D_k^s) \delta(t - D_k^s). \quad (25)$$

Here, $\delta(t)$ is the unit impulse function.

Then, the average access delay are obtained as

$$E[D_k^s] = \sum_{D_k^s=0}^{+\infty} D_k^s p(D_k^s). \quad (26)$$

3.3 The Analysis of Delay Jitter

The delay jitter is a QoS metric, which is defined as the difference of delay experienced by two successive packets. So delay jitter is showed as

$$J_k = D_k - D_{k-1} \quad (27)$$

where D_k is the delay of packet k . The delay D_k contains both the waiting delay and the service delay. The packets in the queue are served in accordance with the principle of first-in-first-out (FIFO). The waiting delay is defined as the time interval between the arrival moment of the packet and the moment when the packet starts to be served. The service delay is the time to serve a packet. In other words, the service delay is the service time of the packets. For CSMA/CA, the service delay of a packet is random. The arrival moment of packets is random, so delay derivation is

a hard task. As the difference in delay, delay jitter is an even hard issue to be handed. We analyze the delay jitter by a new approach.

For the analysis of delay jitter, we analyze the delay jitter of packets from two situations, which are the empty case and the non-empty case, respectively. In this paper, the definition of delay jitter is not the absolute value of delay jitter. Therefore, the delay jitter could be a negative value.

A. The Case of Empty Queue

When the queue is empty, i.e. there is no packet in the queue, packets can be served immediately after arriving. Hence the delay jitter can be expressed as

$$J_k = D_k^s - D_{k-1}^s \quad (28)$$

where D_k^s is the service time of packet k .

As a result, the statistics of J_k is defined by the joint distribution of two sequential service times $p_{2s}(D_k^s, D_{k-1}^s)$. Based on the statistics, the distribution of difference of two random variables is given as

$$p_{\text{empty}}(J_k) = \int_{-\infty}^{+\infty} p_{2s}(D_{k-1}^s + J_k, D_{k-1}^s) dD_{k-1}^s. \quad (29)$$

According to the EE-CSMA/CA mechanism, the node initializes all parameters after the transmission of a packet. Then, the next packet of the node restarts the competition for the channel. Therefore, we can know that the distribution of service time for packets is independent. So (29) is rewritten as

$$p_{\text{empty}}(J_k) = \int_{-\infty}^{+\infty} p_{\text{service}}(D_{k-1}^s + J_k) p_{\text{service}}(D_{k-1}^s) dD_{k-1}^s. \quad (30)$$

In the subsection B of Sec. 3.2, we obtain the service time distribution of packets. Using the probability distribution of service time, the probability distribution of the delay jitter is given for the empty queue as

$$p_{\text{empty}}(J_k) = \sum_{D_{k-1}^s=0}^{+\infty} p(D_{k-1}^s + J_k) p(D_{k-1}^s). \quad (31)$$

B. The Case of Non-Empty Queue

The situation that the queue is non-empty means that there are packets in the queue. It is assumed that packet $k-1$ arrives at the queue at t_0 ($t_0 < 0$) and starts being served at $t = 0$. Packet k arrives at the queue at $t_0 + \tau$ and starts being served at D_{k-1}^s , as shown in Fig. 3. For the case of the non-empty queue, the packet k has already been in the queue when the packet $k-1$ begins to be served. Here, τ is the inter-arrival time of two sequential packets. The packet $k-1$ and packet k leave the queue at the D_{k-1}^s and $D_{k-1}^s + D_k^s$, respectively. Therefore, the delay jitter could be evaluated as

$$\begin{aligned} J_k &= D_k - D_{k-1} \\ &= (D_k^s + D_{k-1}^s - t_0 - \tau) - (D_{k-1}^s - t_0) \\ &= D_k^s - \tau. \end{aligned} \quad (32)$$

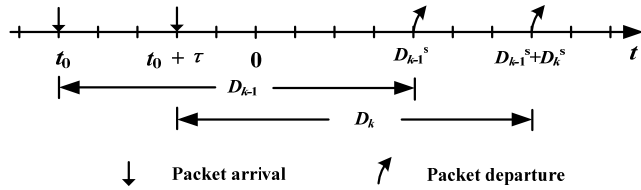


Fig. 3. Queuing analysis.

Delay jitter is related to the inter-arrival time of two sequential packets and the service time of the packet. They are independent, so the probability distribution of delay jitter for the non-empty queue is shown as

$$p_{\text{non-empty}}(J_k) = \int_{-\infty}^{+\infty} p_{\text{service}}(\tau + J_k) p_A(\tau) d\tau \quad (33)$$

where $p_A(\tau)$ is the distribution of the inter-arrival time of two sequential packets. So we can derive delay jitter for the non-empty queue

$$p_{\text{non-empty}}(J_k) = \sum_{D_k^s=0}^{+\infty} p(D_k^s) p_A(D_k^s - J_k). \quad (34)$$

C. The General Case

In the general case, the queue of node is sometimes empty and sometimes not empty. Let l_0 be the probability that the queue is empty. According to the previous analysis, the distribution of delay jitter for the general case can be expressed as follows

$$p_{\text{general}}(J_k) = l_0 p_{\text{empty}}(J_k) + (1 - l_0) p_{\text{non-empty}}(J_k). \quad (35)$$

From Appendix B, l_0 is obtained.

The average delay jitter is defined as expectation of the absolute value of the delay jitter, which is expressed as

$$E[|J_k|] = \sum_{J_k=-\infty}^{+\infty} |J_k| p_{\text{general}}(J_k). \quad (36)$$

3.4 The Minimum Duration of the Active Period Constrained by QoS

In this section, we estimate the minimum duration of the active period constrained by access delay and delay jitter by the dichotomy algorithm, respectively. We have obtained the access delay and the delay jitter in the previous analysis. Different durations of the active period and inactive period have influence on the access delay and the delay jitter. For energy saving and QoS guarantee, it is necessary to design an algorithm to estimate the minimum duration of the active period. The duration of the frame structure is constant and the active period contains integer number of slots. For the simplicity of the algorithm, we adopt the dichotomy method to solve this problem. Considering the access delay constraint, we formulate the optimization problem as

$$\begin{aligned} \min T_1 \\ \text{s.t. } E[D_k^s] \leq \text{Delay} \end{aligned} \quad (37)$$

where $Delay$ is the target of the access delay.

For the delay jitter constraint, the optimization problem is formulated as

$$\begin{aligned} \min T_1 \\ \text{s.t. } E[|J_k|] \leq \text{Jitter} \end{aligned} \quad (38)$$

where $Jitter$ denotes the target of the delay jitter.

In this paper, we adopt the dichotomy algorithm to deal with two optimization problems. As an example, we present the pseudo-code of the dichotomy with access delay constraint in Tab. 3.

```

Input:  $M, macMinBE, macMaxBE, m, n, T, T_p, \lambda;$ 
% Initialization
 $L=0;$  /*lower bound on the duration of the active period */
 $H=T;$  /*upper bound on the duration of the active period */
 $T_i=(L+H)/2;$ 
calculate  $E[D_k^s]$  according to (26)
% using dichotomy to find the minimum duration of active
% period that can satisfy the access delay constrain
while  $|E[D_k^s] - Delay| > \delta$  /*  $\delta$  is acceptable error */
  if  $E[D_k^s] > Delay$ 
     $L=T_i;$  /*increase the duration of the active period */
  end if
  if  $E[D_k^s] < Delay$ 
     $H=T_i;$  /*reduce the duration of the active period */
  end if
   $T_i=(L+H)/2;$ 
  calculate  $E[D_k^s]$  according to (26)
end while
Output:  $T_1.$ 
    
```

Tab. 3. The dichotomy algorithm.

4. Simulations and Results Analysis

In this section, we present the theoretical results and simulated results of access delay and delay jitter. The simulated results are obtained using the MATLAB, where we simulate the EE-CSMA/CA mechanism. And then we calculate the probability distribution of service delay and delay jitter. The parameters used in the theoretical results and simulated results are listed in Tab. 4. We consider the arrival of packets is the Poisson distribution. The CSMA/CA mechanism with inactive period is suitable for a system with light load. Packets arrive at nodes with the average rate λ (the number of packets per slot).

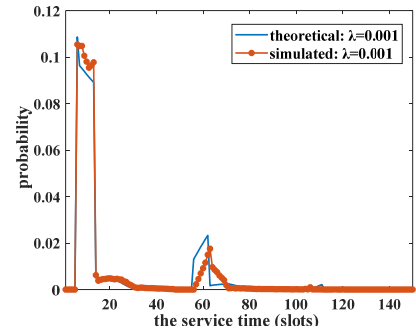
The number of nodes M	5
Initial backoff window W_0	8
$macMinBE$	3
$macMaxBE$	5
The maximum backoff stage m	4
The maximum retransmission times n	3
Packet payload	4000 bits
MAC header	272 bits
PHY header	120 bits
Data bit rate	54 Mbit/s
Slot time	20 μ s

Tab. 4. Parameters used in the simulations.

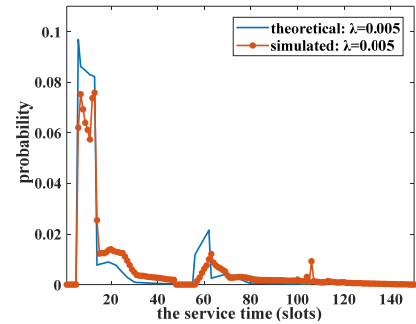
Figure 4 presents the service time distribution of packets in several cases. Both the durations of active and inactive period in the frame structure are set as 50 slots in Fig. 4. It is found that the service time of packets grows along with the arrival rate of packets. The arrival rate of packets increases, the competition in the networks is fierce. The probability that the channel is busy and the collision probability of a packet increase, which results in the increase of the times of backoff or the times of retransmission. Therefore, the service time of packets increases. It is observed that the service time of packets may be around 60 slots in Fig. 4. The reason is that the packet is not served completely in the current active period, and it waits for an inactive period, then continuously served in the next active period. The theoretical results and simulated results are basically the same in Fig. 4, but there exist some differences between them. It may be caused by the approximation in modeling. It is assumed that service process could enter the inactive state from any states of backoff operation with the same transition probability. We are interesting in refining the model in our next step study.

Figure 5 shows the average access delay of packets under the different number of nodes and various frame structures. The average access delay goes up along with the number of nodes. Meanwhile, it is presented that the average access delay goes down as the active ratio R increases. When the duration of active period becomes bigger, nodes may be served within the current active period. Therefore, the performance of average access delay is better. In contrast to the CSMA/CA mechanism with the inactive period, the conventional CSMA/CA mechanism has better access delay performance at the cost of energy consumption. The three curves bound by the dotted circle are the case that the active ratio R is set to 0.5. In this case, their performance of average access delay is similar.

Figure 6 presents the probability distribution of the delay jitter. Both the durations of active and inactive period in the frame structure are set as 50 slots in Fig. 6. When the arrival rate is small, the theoretical results and simulated results are basically the same. There is a slight gap between the theoretical results and the simulated results when the arrival rate is big. When the arrival rate of packets is the lower, the values of delay jitter are small and centered around 0, 50 and -50. In this situation, since no packets are waiting in the queue. The delay jitter is caused by the difference between the service time of two consecutive packets. As shown in Fig. 4, with a higher probability, the service time of packets takes 10 and 60 slots. As a result, the absolute value of delay jitter takes 0 and 50 slots with a higher probability. When the arrival rate of packets is the higher, the queue of nodes is not empty. It is found that the performance of delay jitter gets worse. In fact, when the traffic of services is large, the performance of delay jitter is poor. It can be seen that the service delay of packets increases with the arrival rate of packets from Fig. 4. The performance of delay jitter deteriorates. Values of delay jitter cover a big space of values.



(a) The probability distribution of service time ($\lambda = 0.001$).



(b) The probability distribution of service time ($\lambda = 0.005$).

Fig. 4. The probability distribution of service time under different arrival rates.

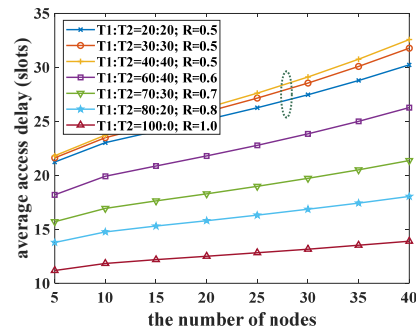


Fig. 5. The average access delay versus the number of nodes and the frame structure ($\lambda = 0.003$).

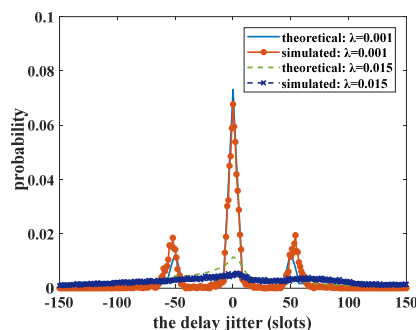


Fig. 6. The probability distribution of delay jitter.

Figure 7 shows the impacts of the number of nodes and the several frame structures on the average delay jitter. It is obvious that the average delay jitter rises with the number of nodes. When the active ratio R increases, the performance of delay jitter gets better. When the duration

of active period increases and the duration of frame remains unchanged, the service time of packets shortens. Further, the performance of access delay gets better, and then average delay jitter decreases. The longer the active period is, the better the delay jitter performance is, but the more energy consumption is. The three curves bound by the dotted circle are the case that the active ratio R is set to 0.5. When the active ratio R is set to be 0.5, the average delay jitter increases along with the duration of the active period. That is, the duration of the active period affects the delay jitter when the active ratio R is constant.

In Fig. 5 and Fig. 7, it is presented that the access delay and the delay jitter vary with configurations of the frame structure. Under the access delay or delay jitter constraints (25 slots), we estimate the suitable duration of the active period by the dichotomy in Fig. 8. Under different number of nodes and arrival rate of packets, we show the suitable duration of the active period constrained by the access delay in Fig. 8(a), and constrained by the delay jitter

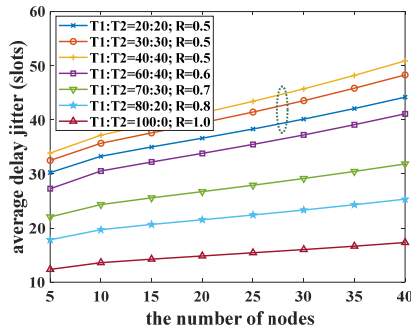
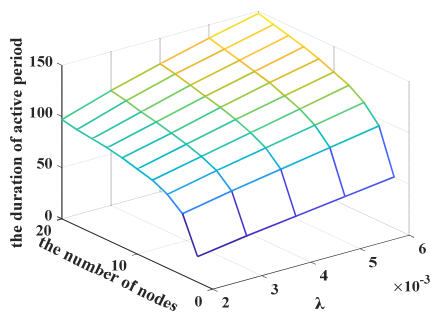
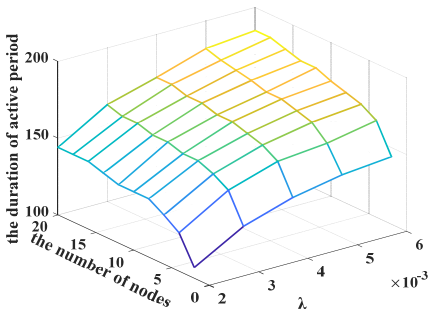


Fig. 7. The average delay jitter versus the number of nodes and the frame structure ($\lambda = 0.003$).



(a) The duration of active period under access delay constraint.



(b) The duration of active period under delay jitter constraint.

Fig. 8. The duration of active period constrained by QoS.

in Fig. 8(b). A frame is set to be 200 slots in Fig. 8. With the increase of the arrival rate of packets or the number of nodes, the service time of packets increase. The performance of access delay and delay jitter degrades. To ensure the access delay or delay jitter constraint, the duration of the active period increases. It means that the more nodes or the higher arrival rate are, the longer the needed active period is.

5. Conclusion

In this paper, we modeled the CSMA/CA mechanism with the consideration of its active period and inactive period. We derived the probability distribution of the service time in probability generation domain. Then, we decoupled the queue in a node and configured two logical queues, which helped to enable the complex analysis of delay jitter. Queuing theory based analysis indicated that the delay jitter was associated with the arrival and the service. Based on the whole probability analysis, the probability distribution of the delay jitter is obtained. The simulation results showed that both of the duration of active period and inactive period (i.e., frame structure) had effect on the access delay and the delay jitter. For energy saving and QoS guarantee, we found the suitable duration of the active period by the dichotomy under the constraints of access delay or delay jitter. It is interesting to explore the delay jitter of real-time service in the networks, especially when it coexists with other several classes of services. This is our next research subject.

Appendix A

Considering the random binary exponential backoff process, the contention window size at the i -th stage is shown as follows:

$$W_i = \begin{cases} 2^i W_0 & i \leq s \\ 2^s W_0 & i > s \end{cases} \tag{39}$$

where $W_0 = 2^{macMinBE}$, $s = macMaxBE - macMinBE$. And parts of transition probabilities are shown as follows.

$$P\{i+1, j, r, 0 | i, -1, r, 0\} = \frac{\alpha + (1-\alpha)\beta}{W_{i+1}},$$

$$i \in [0, m-1], j \in [0, W_{i+1}-1], r \in [0, n],$$

$$P\{0, j, 0, 0 | i, -1, r, 0\} = \frac{(1-\alpha)(1-\beta)(1-P_c)}{W_0},$$

$$i \in [0, m], j \in [0, W_0-1], r \in [0, n-1],$$

$$P\{0, j, 0, 0 | i, -1, n, 0\} = \frac{(1-\alpha)(1-\beta)}{W_0},$$

$$i \in [0, m-1], j \in [0, W_0-1], r = n,$$

$$P\{0, j, 0, 0 | m, -1, n, 0\} = \frac{1}{W_0}, \quad i = m, j \in [0, W_0-1], r = n,$$

$$\begin{aligned}
 P\{0, j, r+1, 0 | i, -1, r, 0\} &= \frac{(1-\alpha)(1-\beta)P_c}{W_0}, \\
 & i \in [0, m], j \in [0, W_0 - 1], r \in [0, n-1], \\
 P\{0, j, 0, 0 | m, -1, r, 0\} &= \frac{\alpha + (1-\alpha)\beta}{W_0}, \\
 & i = m, j \in [0, W_0 - 1], r \in [0, n-1], \\
 P\{i, -1, r, 0 | i, 0, r, 0\} &= 1, \quad i \in [0, m], r \in [0, n]. \quad (40)
 \end{aligned}$$

The first equation of (40) stands for the transition probability when the node chooses randomly backoff time counter in the next backoff stage after the failed first CCA or the failed second CCA. The second equation of (40) is the transition probability when the node goes to initial state after transmitting successfully. The third and fourth equations of (40) are the transition probabilities when the node goes to initial state after reaching the maximum value of retransmission. The fifth equation of (40) defines the transition probability when the node goes to the next retransmission after transmitting unsuccessfully. The sixth equation of (40) is the transition probability when the node goes to initial state and chooses randomly backoff time counter after the failed first CCA or the failed second CCA in the maximum value of backoff. The seventh equation of (40) expresses that the node senses a channel when the backoff time counter decreases to zero.

Appendix B

In this section, we model the queuing system in any node as a M/G/1/K queue, where K is the capacity of the queue. The number of packets in the queue is a discrete time Markov chain, and the state space of the queuing system is represented as $\{0, 1, \dots, K-1\}$. The transition probability matrix \mathbf{P} is given by

$$\mathbf{P} = \begin{bmatrix} A_0 & A_1 & A_2 & \cdots & A_{K-2} & A'_{K-1} \\ A_0 & A_1 & A_2 & \cdots & A_{K-2} & A'_{K-1} \\ 0 & A_0 & A_1 & \cdots & A_{K-3} & A'_{K-2} \\ \vdots & \vdots & \vdots & \ddots & \vdots & \vdots \\ 0 & 0 & 0 & \cdots & A_0 & A'_1 \end{bmatrix} \quad (41)$$

where A_i and A'_i can be shown as follows

$$A_i = \sum_{D_k^s=1}^{\infty} a(i, D_k^s) p(D_k^s), \quad (42)$$

$$A'_i = 1 - \sum_{j=0}^{i-1} A_j \quad (43)$$

where $a(i, D_k^s)$ represents the probability that there are i packets arriving at the node in the D_k^s time interval, and $p(D_k^s)$ is the probability that the service time of packet k is D_k^s .

The limiting state probability vector of the queue is $\pi = (\pi_0, \pi_1, \dots, \pi_{K-1})$. Based on the Markov theory, it is

obtained. The steady state probability of Markov chain l_i , the mean delay T , and throughput S are as follows

$$l_i = \frac{\pi_i}{\lambda E[D_k^s] + \pi_0}, \quad 0 \leq i \leq K-1, \quad (44)$$

$$l_K = 1 - \sum_0^{K-1} l_i, \quad i = K,$$

$$T = \frac{1}{\lambda(1-l_K)} \left[\sum_{k=0}^{K-1} k\pi_k + K(\lambda E[D_k^s] + \pi_0 - 1) \right], \quad (45)$$

$$S = \lambda(1-l_K) \left(1 - x^{m+1} \frac{1-g^{n+1}}{1-g} b_{0,0,0,0} \right) (1 - P_c(1-x^{m+1})g^n b_{0,0,0,0}). \quad (46)$$

In this paper, we apply the iterative algorithm, and the steps are shown as following steps.

- (1) Initialize $l_0 = 0$, which is the saturated condition.
- (2) Based on l_0 , calculate the P_s .
- (3) Calculate the service time distribution of the packets in the probability generation domain.
- (4) Calculate the steady state probability of the queuing system.
- (5) Calculate the mean delay T and throughput S .
- (6) If T and S converge to the previous value, the algorithm will stop; otherwise, update l_0 and return step 2. The algorithm will repeat until the convergence condition is satisfied.

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